# MMIC REFLECTIONLESS FILTER DICE

50Ω DC to 21 GHz

# The Big Deal

- Patented design eliminates in band spurs
- Pass band cut-off up to 21 GHz
- Stop band up to 35 GHz
- Excellent repeatability through IPD\* process
- Unpackaged Die Form



Mini-Circuits' X-Series reflectionless filters employ a novel filter topology which absorbs and terminates stop band signals internally rather than reflecting them back to the source. This new capability enables unique applications for filter circuits beyond those suited to traditional approaches. Traditional filters are reflective in the stop band, sending signals back to the source at 100% of the power level which interact with neighboring components and often result in intermodulation and other interferences. Reflectionless filters eliminate stop band reflections, allowing them to be paired with sensitive devices and used in applications that otherwise require circuits such as isolators, isolation amplifiers or attenuators.

X-Series

Available in Low Pass, High Pass and Band Pass designs

Key Features	Advantages		
Easy integration with sensitive reflective components, e.g. mixers, multipliers	Reflectionless filters absorb unwanted signals, preventing reflections back to the source. This reduces generation of additional unwanted signals without the need for extra components like attenuators, improving system dynamic range and saving board space.		
Enables stable integration of wideband amplifiers	Because reflectionless filters maintain good impedance in the stop band; they can be integrated with high gain, wideband amplifiers without the risk of creating instabilities in these out of band regions.		
Cascadable	Reflectionless filters can be cascaded in multiple sections to provide sharper and higher attenuation, while also preventing any standing waves that could affect pass band signals.		
Excellent power handling in a tiny surface mount device	High power handling extends the usability of these filters to the transmit path for inter-stage filtering.		
Excellent repeatability of RF performance	Through semiconductor IPD process, X-series filters are inherently repeatable for large volume production.		
Excellent stability over temperature	With ±0.3 dB variation over temperature ideal for use in wide temperature range applications without the need for additional temperature compensation.		
Operating Temperature up to 105°C	Suitable for operation close to high power components		
Unpackaged Die form	Enables direct integration into customer hybrids		

\*IPD - Integrated Passive Device, is a GaAs semiconductor process



# Reflectionless Low Pass Filter Die

# XLF-962-D+

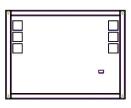
50Ω DC to 9600 MHz

#### Features

- $\bullet$  Match to 50  $\!\Omega$  in the stop band, eliminates undesired reflections
- Cascadable
- Excellent Power handling
- Protected by US Patent No. 8,392,495

#### **Applications**

- Harmonics Rejection
- Satellite
- Radar
- Military & Space



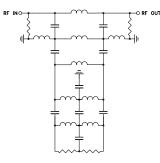
+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

#### **General Description**

Mini-Circuits' XLF-962-D+ reflectionless filter die employs a novel filter topology which absorbs and terminates stop band signals internally rather than reflecting them back to the source. This new capability enables unique applications for filter circuits beyond those suited to traditional approaches. Traditional filters are reflective in the stop band, sending signals back to the source at 100% of the power level. These reflections interact with neighboring components and often result in inter-modulation and other interferences. Reflectionless filters eliminate stop band reflections, allowing them to be paired with sensitive devices and used in applications that otherwise require circuits such as isolation amplifiers or attenuators.

#### **Simplified Schematic**



Pad	Description	
RF-IN	RF Input Pad	
RF-OUT	RF Output Pad	
Ground	Ground Bonding Pad	





#### Electrical Specifications<sup>1</sup> at 25°C

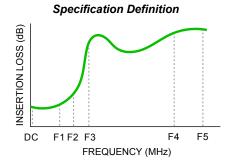
Pa	rameter	F#	Frequency (MHz)	Min.	Тур.	Max.	Unit
	Insertion Loss	DC - F1	DC-9600		1.4		dB
Pass Band	Frequency Cut-off	F2	12400		3.0		dB
	VSWR	DC - F1	DC-9600		1.2		:1
		F3 - F4	14800 - 16000		14		dB
	Rejection	F4 - F5	16000 - 25200		20		dB
Stop Band							:1
	VSWR	F3 - F4	14800 - 16000		1.3		
	-	F4 - F5	16000 - 25200		2.4		:1

<sup>1</sup> Measured on Mini-Circuits Characterization test board. Die packaged in 3mm x 3mm, 12-lead MCLP package and soldered on TB-844-962+

#### Absolute Maximum Ratings<sup>1,4</sup>

Parameter	Ratings
Operating Temperature	-55°C to +105°C
RF Power Input, Passband (DC-F1) <sup>2</sup>	2W at 25°C
RF Power Input, Stopband (F2-F5) <sup>3</sup>	80mW at 25°C

<sup>2</sup> Passband rating derates linearly to 1W at 105°C ambient
 <sup>3</sup> Stopband rating derates linearly to 40mW at 105°C ambient
 <sup>4</sup> Permanent damage may occur if any of these limits are exceeded.



#### Typical Performance Data at 25°C<sup>1</sup>

.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
Frequency (MHz)	Insertion Loss (dB)	VSWR (:1)
10	0.38	1.04
100	0.31	1.02
400	0.30	1.08
800	0.33	1.17
1500	0.39	1.25
2500	0.46	1.23
3500	0.49	1.16
5000	0.61	1.22
6500	0.72	1.19
8000	0.88	1.16
9600	1.35	1.54
11000	2.10	1.86
12400	2.89	1.40
14800	14.50	1.37
16000	28.82	1.18
18000	23.70	2.65
20000	20.53	2.37
22000	25.19	1.69
24000	22.94	2.90
25200	19.51	3.79
INSERTION LOSS		VSWR
60	5.0	
(B) 45 30 15	4.0	
0 <sup>45</sup>	4.0	
SC	n n l	
30	3.0	
	$\checkmark$ $\land$ $\land$	
版 15	2.0	
SZ I I I I I I I I I I I I I I I I I I I		
	1.0	
0 5010 10020 15120		5040 10080 15120 20160 25200
0 5040 10080 15120 2	20160 25200 0	3040 10000 13120 20100 23200

# lMini-Circuits

FREQUENCY (MHz)

www.minicircuits.com P.O. Box 350166, Brooklyn, NY 11235-0003 (718) 934-4500 sales@minicircuits.com

FREQUENCY (MHz)





920 1000

## **Die Layout**

# **Bonding Pad Position**

(Dimensions in  $\mu$ m, Typical)

726 617.5

517.5 417.5

0

0 80

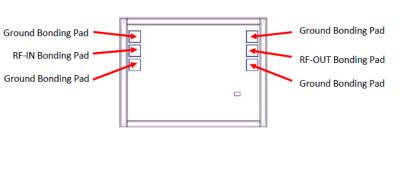




Fig 2. Bonding Pad Positions

### **Critical Dimensions**

Parameter	Values
Die Thickness, µm	100
Die Width, μm	1000
Die Length, µm	726
Bond Pad Size (Ground pad), µm	75 x 75



#### Assembly and Handling Procedure

- 1. Storage
  - Dice should be stored in a dry nitrogen purged desiccators or equivalent.
- 2. ESD

MMIC Gallium Arsenide (GaAs) filter dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static worksta tion. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.

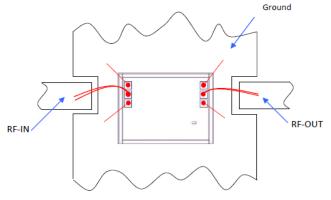
3. Die Attach

The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030Hk-PT/H579/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufac turer's cure condition. It is recommended to use antistatic die pick up tools only.

4. Wire Bonding

Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.

## Assembly Diagram



#### **Recommended Wire Length, Typical**

Wire	Wire Length (mm)	Wire Loop Height (mm)
All wires	1.0	0.15

Note: Use double bond wire at RF IN & RF OUT



Additional Detailed Technica additional information is available on our o			
	Data Table		
Performance Data	Swept Graphs		
	S-Parameter (S2P Files) Data Set with and without port extension(.zip file)		
Case Style	Die		
Die Ordering and packaging	Quantity, Package	Model No.	
	Small, Gel - Pak: 10,50,100 KGD* Medium <sup>†</sup> , Partial wafer: KGD*<1745	XLF-962-DG+ XLF-962-DP+	
information	<sup>†</sup> Available upon request contact sales representative		
	Refer to <u>AN-60-067</u>		
Environmental Ratings	ENV-80		

\*Known Good Dice ("KGD") means that the dice are taken from PCM good wafer and visually inspected according to Mini-Circuits inspection criteria. While this is not definitive, it does help to provide a higher degree of confidence that dice are capable of meeting typical RF electrical parameters specified by Mini-Circuits.

ESD Rating\*\*

Human Body Model (HBM): Class 1A (250V to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

\*\* Tested in industry standard MCLP 3x3mm 12 lead package.

#### **Additional Notes**

- A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
- B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuit's applicable established test performance criteria and measurement instructions.
- C. The parts covered by this specification document are subject to Mini-Circuits standard limited warranty and terms and conditions (collectively, "Standard Terms"); Purchasers of this part are entitled to the rights and benefits contained therein. For a full statement of the Standard Terms and the exclusive rights and remedies thereunder, please visit Mini-Circuits' website at www.minicircuits.com/MCLStore/terms.jsp
- D. Mini-Circuits does not warrant the accuracy or completeness of the information, text, graphics and other items contained within this document and same are provided as an accommodation and on an "As is" basis, with all faults.
- E. Purchasers of this part are solely responsible for proper storing, handling, assembly and processing of Known Good Dice (including, without limitation, proper ESD preventative measures, die preparation, die attach, wire bond ing and related assembly and test activities), and Mini-Circuits assumes no responsibility therefor or for environmental effects on Known Good Dice.
- F. Mini-Circuits and the Mini-Circuits logo are registered trademarks of Scientific Components Corporation d/b/a Mini-Circuits. All other third-party trademarks are the property of their respective owners. A reference to any third-party trademark does not constitute or imply any endorsement, affiliation, sponsorship, or recommendation by any such third-party of Mini-Circuits or its products.

