

Engineering Development Model

Power Splitter/Combiner ZAPD-ED10128A/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

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CASE STYLE : 99-01-916

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		1150		2000 MHz
Isolation	1150 - 2000 MHz		25	
Insertion Loss Above 3.0 dB	1150 - 2000 MHz		0.40	
Phase Unbalance	1150 - 2000 MHz		1.913	
Amplitude Unbalance	1150 - 2000 MHz		0.042	
VSWR	SUM Port		1.20	(:1)
	OUT Ports		1.20	(:1)

MAXIMUM RATINGS	
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	J1
PORT 1	J2
PORT 2	J3

Functional Diagram

