## Engineering Development Model

# Power Splitter/Combiner ZAPD-ED11331/1

2 Way-0°

### **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

**CASE STYLE: F14** 

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C						
Parameter		Min.	Тур.	Max.	Units	
Frequency		20		1000	MHz	
Isolation	20 - 200 MHz		23		dB	
	200-500 MHz		22		dB	
	500-1000 MHz		28		dB	
Insertion Loss Above 3.0 dB	20 - 200 MHz		0.20		dB	
	200-500 MHz		0.25		dB	
	500-1000 MHz		0.60		dB	
Phase Unbalance	20 - 200 MHz		0.01		deg.	
	200-500 MHz		0.05		deg.	
	500-1000 MHz		0.08		deg.	
Amplitude Unbalance	20 - 200 MHz		0.01		dB	
	200-500 MHz		0.01		dB	
	500-1000 MHz		0.04		dB	
VSWR	SUM Port		1.18		(:1)	
	OUT Ports		1.25		(:1)	

MAXIMUM RATINGS			
Operating Temperature	-55°C to 100°C		
Storage Temperature	-55°C to 100°C		

PIN CONNECTIONS			
SUM PORT	S, TNC FEMALE		
PORT 1	1, TNC FEMALE		
PORT 2	2, TNC FEMALE		

# **Functional Diagram**

