## Engineering Development Model

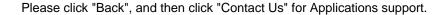
## Power Splitter/Combiner

## **ZAPD-ED11923**

2 Way-0°

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.





**CASE STYLE: 99-01-908** 

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		Min.	Тур.	Max.	Units
Frequency		1650		2200	MHz
Isolation	1650 - 2200 MHz		27		dB
Insertion Loss Above 3.0 dB	1650 - 2200 MHz		0.25		dB
Phase Unbalance	1650 - 2200 MHz		0.979		deg.
Amplitude Unbalance	1650 - 2200 MHz		0.012		dB
VSWR	SUM Port		1.14		(:1)
	OUT Ports		1.11		(:1)

MAXIMUM RATINGS			
Operating Temperature	-40°C to 85°C		
Storage Temperature	-55°C to 100°C		

PIN CONNECTIONS			
SUM PORT	S		
PORT 1	1		
PORT 2	2		

