Engineering Development Model

Power Splitter/Combiner ZAPD-ED12254/2

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE: F53

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		Min.	Тур.	Max.	Units
Frequency		500		2000	MHz
Isolation	500 - 2000 MHz		28		dB
Insertion Loss Above 3.0 dB	500 - 2000 MHz		0.60		dB
Phase Unbalance	500 - 2000 MHz		0.241		deg.
Amplitude Unbalance	500 - 2000 MHz		0.010		dB
VSWR	SUM Port		1.21		(:1)
	OUT Ports		1.15		(:1)

MAXIMUM RATINGS				
Operating Temperature	-55°C to 100°C			
Storage Temperature	-55°C to 100°C			

PIN CONNECTIONS				
SUM PORT	S			
PORT 1	1			
PORT 2	2			

Functional Diagram

