*Engineering Development Model*

# Power Splitter/Combiner ZAPD-ED12592C/1

2 Way-0°

### Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

**CASE STYLE : F53**

| ELECTRICAL SPECIFICATIONS 50Ω @ +25°C | | | | | |
|---|---|---|---|---|---|
| **Parameter** | | **Min.** | **Typ.** | **Max.** | **Units** |
| **Frequency** | | 200 | | 6400 | MHz |
| **Isolation** | **200 - 2000 MHz** | | 15 | | dB |
| | **2000 - 3200 MHz** | | 30 | | dB |
| | **3200 - 6400 MHz** | | 20 | | dB |
| **Insertion Loss Above 3.0 dB** | **200 - 2000 MHz** | | 0.40 | | dB |
| | **2000 - 3200 MHz** | | 0.55 | | dB |
| | **3200 - 6400 MHz** | | 0.87 | | dB |
| **Phase Unbalance** | **200 - 2000 MHz** | | 0.07 | | deg. |
| | **2000 - 3200 MHz** | | 0.11 | | deg. |
| | **3200 - 6400 MHz** | | 0.35 | | deg. |
| **Amplitude Unbalance** | **200 - 2000 MHz** | | 0.02 | | dB |
| | **2000 - 3200 MHz** | | 0.03 | | dB |
| | **3200 - 6400 MHz** | | 0.11 | | dB |
| **VSWR** | **SUM Port** | | 1.23 | | (:1) |
| | **OUT Ports** | | 1.31 | | (:1) |

| MAXIMUM RATINGS | |
|---|---|
| **Operating Temperature** | -55°C to 100°C |
| **Storage Temperature** | -55°C to 100°C |

| PIN CONNECTIONS | |
|---|---|
| **SUM PORT** | S |
| **PORT 1** | 1 |
| **PORT 2** | 2 |

## Functional Diagram