Engineering Development Model

Power Splitter/Combiner ZAPD-ED12950/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : F14

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		1		1200	MHz
Isolation	**1 - 10 MHz**		24		dB
	10 - 600 MHz		24		dB
	600 - 1200 MHz		18		dB
Insertion Loss Above 3.0 dB	**1 - 10 MHz**		0.35		dB
	10 - 600 MHz		0.45		dB
	600 - 1200 MHz		0.75		dB
Phase Unbalance	**1 - 10 MHz**		0.046		deg.
	10 - 600 MHz		0.073		deg.
	600 - 1200 MHz		0.245		deg.
Amplitude Unbalance	**1 - 10 MHz**		0.009		dB
	10 - 600 MHz		0.014		dB
	600 - 1200 MHz		0.029		dB
VSWR	**SUM Port**		1.22		(:1)
	OUT Ports		1.21		(:1)

MAXIMUM RATINGS	
Operating Temperature	-40°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	S
PORT 1	1
PORT 2	2

Functional Diagram