

Engineering Development Model

Power Splitter/Combiner

ZASC-ED8005/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : F14

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		1		500 MHz
Isolation	1 - 10 MHz		28	dB
	10 - 250 MHz		29	dB
	250 - 500 MHz		26	dB
Insertion Loss Above 3.0 dB	1 - 10 MHz		0.34	dB
	10 - 250 MHz		0.35	dB
	250 - 500 MHz		0.56	dB
Phase Unbalance	1 - 10 MHz		0.020	deg.
	10 - 250 MHz		0.192	deg.
	250 - 500 MHz		0.597	deg.
Amplitude Unbalance	1 - 10 MHz		0.000	dB
	10 - 250 MHz		0.002	dB
	250 - 500 MHz		0.003	dB
VSWR	SUM Port		1.04	(:1)
	OUT Ports		1.12	(:1)

MAXIMUM RATINGS	
Operating Temperature	-55°C to 85°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	S
PORT 1	1
PORT 2	2

Functional Diagram

