## Engineering Development Model

## Power Splitter/Combiner ZBSC-ED7451/1

2 Way-0°

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

**CASE STYLE: F14** 

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		Min.	Тур.	Max.	Units
Frequency		10		3200	MHz
Isolation	10 - 100 MHz		15		dB
	100 - 1600 MHz		16		dB
	1600 - 3200 MHz		19		dB
Insertion Loss Above 3.0 dB	10 - 100 MHz		1.07		dB
	100 - 1600 MHz		1.11		dB
	1600 - 3200 MHz		1.43		dB
Phase Unbalance	10 - 100 MHz		0.024		deg.
	100 - 1600 MHz		0.368		deg.
	1600 - 3200 MHz		1.236		deg.
Amplitude Unbalance	10 - 100 MHz		0.019		dB
	100 - 1600 MHz		0.029		dB
	1600 - 3200 MHz		0.185		dB
VSWR	SUM Port		1.55		(:1)
	<b>OUT Ports</b>		1.60		(:1)

MAXIMUM RATINGS				
Operating Temperature	-55°C to 85°C			
Storage Temperature	-55°C to 100°C			

PIN CONNECTIONS			
SUM PORT	S		
PORT 1	1		
PORT 2	2		

