Engineering Development Model

Power Splitter/Combiner ZFSC-ED13009/1

8 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : UU1268
Photo for reference only

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		1		800	MHz
Isolation	**1 - 10 MHz**		41		dB
	10 - 400 MHz		45		dB
	400 - 800 MHz		46		dB
Insertion Loss Above 9.0 dB	**1 - 10 MHz**		0.66		dB
	10 - 400 MHz		0.70		dB
	400 - 800 MHz		1.40		dB
Phase Unbalance	**1 - 10 MHz**		0.30		deg.
	10 - 400 MHz		0.85		deg.
	400 - 800 MHz		2.30		deg.
Amplitude Unbalance	**1 - 10 MHz**		0.07		dB
	10 - 400 MHz		0.07		dB
	400 - 800 MHz		0.17		dB
VSWR	**SUM Port**		1.13		(:1)
	OUT Ports		1.07		(:1)

MAXIMUM RATINGS	
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	S, TNC, FEMALE
PORT 1	1, TNC, FEMALE
PORT 2	2, TNC, FEMALE
PORT 3	3, TNC, FEMALE
PORT 4	4, TNC, FEMALE
PORT 5	5, TNC, FEMALE
PORT 6	6, TNC, FEMALE
PORT 7	7, TNC, FEMALE
PORT 8	8, TNC, FEMALE

Functional Diagram