## Engineering Development Model

## Power Splitter/Combiner ZN2PD-ED12512/1

2 Way-0°

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

**CASE STYLE: VVV180** 

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C						
Parameter		Min.	Тур.	Max.	Units	
Frequency		1800		6100	MHz	
Isolation	1800 - 6100 MHz		24		dB	
Insertion Loss Above 3.0 dB	1800 - 6100 MHz		0.45		dB	
Phase Unbalance	1800 - 6100 MHz		0.313		deg.	
Amplitude Unbalance	1800 - 6100 MHz		0.082		dB	
VSWR	SUM Port		1.25		(:1)	
	OUT Ports		1.20		(:1)	

MAXIMUN	<u>Functiona</u>	<u>l Diagram</u>	
Operating Temperature	-40°C to 85°C		PORT
Storage Temperature	-55°C to 100°C		
PIN CONI	NECTIONS	PORT S	PORT
SUM PORT	S		
PORT 1	1		
PORT 2	2		

Page 1 of 1