

Engineering Development Model

Power Splitter/Combiner ZN2PD2-ED12592/1

2 Way-0°

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



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CASE STYLE : VVV845

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C				
Parameter		Min.	Typ.	Max. Units
Frequency		350		6000 MHz
Isolation	350 - 3500 MHz		25	dB
	3000 - 6000 MHz		22	dB
Insertion Loss Above 3.0 dB	350 - 3500 MHz		0.43	dB
	3000 - 6000 MHz		0.80	dB
Phase Unbalance	350 - 3500 MHz		0.214	deg.
	3000 - 6000 MHz		1.097	deg.
Amplitude Unbalance	350 - 3500 MHz		0.051	dB
	3000 - 6000 MHz		0.062	dB
VSWR	SUM Port		1.50	(:1)
	OUT Ports		1.35	(:1)

MAXIMUM RATINGS	
Operating Temperature	-55°C to 100°C
Storage Temperature	-55°C to 100°C

PIN CONNECTIONS	
SUM PORT	S
PORT 1	1
PORT 2	2

Functional Diagram

