Engineering Development Model

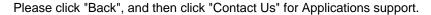
Power Splitter/Combiner

ZN3PD-ED10853/1

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Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.





CASE STYLE: 99-01-965

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C					
Parameter		Min.	Тур.	Max.	Units
Frequency		650		1000	MHz
Isolation	650 - 1000 MHz		29		dB
Insertion Loss Above 4.8 dB	650 - 1000 MHz		0.20		dB
Phase Unbalance	650 - 1000 MHz		0.90		deg.
Amplitude Unbalance	650 - 1000 MHz		0.05		dB
VSWR	SUM Port		1.17		(:1)
	OUT Ports		1.05		(:1)

MAXIMUM RATINGS			
Operating Temperature	-55°C to 100°C		
Storage Temperature	-55°C to 100°C		

PIN CONNECTIONS			
SUM PORT	J1, SMA, FEMALE		
PORT 1	J2, SMA, FEMALE		
PORT 2	J3, SMA, FEMALE		
PORT 3	J4, SMA, FEMALE		

Functional Diagram

