Engineering Development Model

Frequency Synthesizer

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

ZSN-EDR10802/1



CASE STYLE: KG1714

ELECTRICAL SPECIFICATIONS 50Ω, over -15°C to +75°C				
Parameter	Min.	Тур.	Max.	Units
Frequency	850	-	850	MHz
Step size		10000		kHz
Settling Time Within ±1kHz		205		μsec
Output Power	+13	+17	+20	dBm
Phase Noise				
at 100 Hz offset	i I	-99		dBc/Hz
at 1 kHz offset	t l	-108	-102	dBc/Hz
at 10 KHz offset	t	-106	-100	dBc/Hz
at 100 KHz offset	t	-126	-121	dBc/Hz
at 1000 kHz offset	i I	-155	-150	dBc/Hz
Integrated SSB Phase Noise		-60		dBc
Ref & Comp Spurious Suppression		-94		dBc
Non-Harm. Spurious Suppression		-90		dBc
Harmonic Suppression		-61	-55	dBc
Supply voltage VCO & PLL		12		V
Supply current VCO & PLL		9	17	V
Frequency		10		MHz
Reference In Amplitude		1		Vp-p
(External) Impedance		100		kΩ
Ph. N @ 1kHz		-145		dBc/Hz
Digital Lock Locked	11.6		12	V
Detect Unlocked			0.4	V V
Frequency Synthesizer PLL		Self-programmed (inter	nal microcontroller)	

ABSOLUTE MAXIMUM RATINGS			
Operating Temperature	-40°C to 85°C		
Storage Temperature	-55°C to 100°C		
VCO Supply Voltage	13V		
Reference Frequency voltage	3.6Vp-p		

CONNECTIONS		
RF OUT	SMA (Female)	
REF IN	SMA (Female)	
VCC	Turret Terminal	
LOCK DETECT	Turret Terminal	