## Engineering Development Model

## **Frequency Synthesizer**

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

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## ZSN-EDR10920/1



**CASE STYLE: KF1336** 

ELECTRICAL SPECIFICATIONS 50Ω, over -40°C to +75°C							
Parameter	Min.	Тур.	Max.	Units			
Frequency	4600 4950		4600 4950	MHz			
Step size		5000		kHz			
Settling Time Within ±1kHz		40		μsec			
Output Power	-8	-4	+1	dBm			
Phase Noise							
at 100 Hz offset		-75		dBc/Hz			
at 1 kHz offset	t	-85		dBc/Hz			
at 10 KHz offset		-89	-83	dBc/Hz			
at 100 KHz offset	t	-102	-96	dBc/Hz			
at 1000 kHz offset	t	-130	-124	dBc/Hz			
Integrated SSB Phase Noise		-41		dBc			
Reference Spurious Suppression		-102		dBc			
Comparison Spurious Suppression		-74		dBc			
0.5 Step size Spurious Suppression		-94		dBc			
Non-Harm. Spurious Suppression		-90		dBc			
Harmonic Suppression		-64		dBc			
Supply voltage VCO & PLL		15		V			
Supply current VCO & PLL		212	224	V			
Frequency		20		MHz			
Reference In Amplitude		1		Vp-p			
(Internal) Impedance		100		kΩ			
Ph. N @ 1kHz		-145		dBc/Hz			
Digital Lock Locked	2.93		3.33	V			
Detect Unlocked			0.4	V			
Frequency Synthesizer PLL		Self-programmed (internal microcontroller)					

ABSOLUTE MAXIMUM RATINGS					
Operating Temperature	-45°C to 85°C				
Storage Temperature	-55°C to 100°C				
Supply Voltage	16V				

D-Sub15(Male) Pin connections			Other Connections		
VCC	1	LOCK DETECT	6	RF OUT	SMA (Female)
ON / OFF	2	GROUND	9,14		
FREQ.SELECT	3				
NOT CONNECTED	4,5,7,8,10 - 13,15				