Engineering Development Model

Frequency Synthesizer

ZSN-EDR10932/1

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability.
At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

CASE STYLE : KG1714

ELECTRICAL SPECIFICATIONS 50Ω, over -40°C to +70°C					
Parameter		**Min.**	**Typ.**	**Max.**	**Units**
Frequency		1312.5		1312.5	MHz
Step size			31250		kHz
Settling Time	**Within ±1kHz**		2		msec
Output Power		+7	+11	+14	dBm
Phase Noise					
	at 100 Hz offset		-95		dBc/Hz
	at 1 kHz offset		-102	-96	dBc/Hz
	at 10 KHz offset		-110	-104	dBc/Hz
	at 100 KHz offset		-120	-114	dBc/Hz
	at 1000 kHz offset		-148	-142	dBc/Hz
Integrated SSB Phase Noise			-58		dBc
Comparison Spurious Suppression			-31		dBc
Non-Harm. Spurious Suppression			-90		dBc
Harmonic Suppression			-72	-65	dBc
Supply voltage	**VCO & PLL**		5		V
Supply current	**VCO & PLL**		143	152	V
Reference In (External)	**Frequency**		156.25		MHz
	Amplitude		1		Vp-p
	Impedance		100		kΩ
	Ph. N @ 1kHz		-145		dBc/Hz
Digital Lock Detect	**Locked**	2.9		3.3	V
	Unlocked			0.4	
Frequency Synthesizer PLL		Self-programmed (internal microcontroller)			

ABSOLUTE MAXIMUM RATINGS	
Operating Temperature	-45°C to 85°C
Storage Temperature	-55°C to 100°C
Supply Voltage	6V
Reference Frequency voltage	3.6Vp-p

CONNECTIONS	
RF OUT	**SMA (Female)**
REF IN	**SMA (Female)**
VCC	**Turret Terminal**
LOCK DETECT	**Turret Terminal**