Engineering Development Model

Frequency Synthesizer

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.

Please click "Back", and then click "Contact Us" for Applications support.

ZSN-EDR10978



CASE STYLE: KG1714

ELECTRICAL SPECIFICATIONS 50Ω, over -40°C to +70°C					
Parameter		Min.	Тур.	Max.	Units
Frequency		1625		1625	MHz
Step size			15625		kHz
Settling Time With	nin ±1kHz		2.5		msec
Output Power		+7	+11	+15	dBm
Phase Noise					
	at 100 Hz offset		-91		dBc/Hz
	at 1 kHz offset		-99	-94	dBc/Hz
	at 10 KHz offset		-106	-100	dBc/Hz
	at 100 KHz offset		-116	-111	dBc/Hz
	at 1000 kHz offset		-146	-141	dBc/Hz
Integrated SSB Phase Noise			-56		dBc
Reference Spurious Suppression			-94		dBc
Comparison Spurious Suppression			-36		dBc
Non-Harm. Spurious Suppression			-90		dBc
Harmonic Suppression			-72	-63	dBc
Supply voltage	VCO & PLL		5		V
Supply current	VCO & PLL		130	142	V
	Frequency		156.25		MHz
Reference In	Amplitude		1		Vp-p
(External)	Impedance		100		kΩ
	Ph. N @ 1kHz		-145		dBc/Hz
Digital Lock	Locked	2.9		3.3	V
Detect	Unlocked			0.4	V
Frequency Synthesizer P	LL	•	Self-programmed (interi	nal microcontroller)	

ABSOLUTE MAXIMUM RATINGS				
Operating Temperature	-45°C to 85°C			
Storage Temperature	-55°C to 100°C			
Supply Voltage	6V			
Reference Frequency voltage	3.6Vp-p			

CONNECTIONS				
RF OUT	SMA (Female)			
REF IN	SMA (Female)			
VCC	Turret Terminal			
LOCK DETECT	Turret Terminal			