

Engineering Development Model

Frequency Synthesizer

ZSN-EDR11130

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



CASE STYLE : KF1336

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ELECTRICAL SPECIFICATIONS 50Ω, over -10°C to +75°C				
Parameter	Min.	Typ.	Max.	Units
Frequency	1200		1400	MHz
Step size		500		kHz
Settling Time Within ±1kHz		80		μsec
Output Power	+14	+18	+22	dBm
Phase Noise				
at 100 Hz offset		-84		dBc/Hz
at 1 kHz offset		-93		dBc/Hz
at 10 KHz offset		-94	-88	dBc/Hz
at 100 KHz offset		-94	-88	dBc/Hz
at 1000 kHz offset		-134	-128	dBc/Hz
Integrated SSB Phase Noise		-41		dBc
Reference Spurious Suppression		-80		dBc
Comparison Spurious Suppression		-110		dBc
Non-Harm. Spurious Suppression		-90		dBc
Harmonic Suppression		-32		dBc
Supply voltage VCO & PLL		15		V
Supply current VCO & PLL		163	174	mA
Reference In (Internal)				
Frequency		10		MHz
Amplitude		1		Vp-p
Impedance		100		kΩ
Ph. N @ 1kHz		-145		dBc/Hz
Input Logic Levels	Logic high	2.64	3.3	V
Logic Low			0.66	
Digital Lock Detect	Locked	2.9	3.3	V
Unlocked			0.4	
Frequency Synthesizer PLL	ADF4113			

ABSOLUTE MAXIMUM RATINGS	
Operating Temperature	-45°C to 85°C
Storage Temperature	-55°C to 100°C
Supply Voltage	16V
Data, Clock & LE levels	3.6V

D-Sub15(Male) Pin connections				Other Connections	
VCC	1	CLOCK	4	RF OUT	SMA (Female)
MODULATION	7	DATA	3		
LOCK DETECT	6	LATCH ENABLE	5		
NOT CONNECTED	2,7,8,10-13,15	GROUND	9,14		

Notes

A. Performance and quality attributes and conditions not expressly stated in this specification document are intended to be excluded and do not form a part of this specification document.
B. Electrical specifications and performance data contained in this specification document are based on Mini-Circuits' applicable established test performance criteria and measurement instructions.
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