## Engineering Development Model

## Power Splitter/Combiner

## ZX-ED10227/1

2 Way-0°

## **Important Note**

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



Please click "Back", and then click "Contact Us" for Applications support.

**CASE STYLE: 99-01-910** 

ELECTRICAL SPECIFICATIONS 50Ω @ +25°C						
Parameter		Min.	Тур.	Max.	Units	
Frequency		8000		12000	MHz	
Isolation	8000 - 12000 MHz		25		dB	
Insertion Loss Above 3.0 dB	8000 - 12000 MHz		0.30		dB	
Phase Unbalance	8000 - 12000 MHz		2.180		deg.	
Amplitude Unbalance	8000 - 12000 MHz		0.027		dB	
VSWR	SUM Port		1.30		(:1)	
	OUT Ports		1.30		(:1)	

MAXIMU	<u>Functiona</u>	Functional Diagram		
Operating Temperature	-40°C to 85°C		PORT 1	
Storage Temperature	-55°C to 100°C	PORT S		
PIN CONNECTIONS			PORT 2	
SUM PORT	S			
PORT 1	1			
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