Engineering Development Model

Power Splitter/Combiner

ZX10-ED13665/3

2 Way-0°

Parameter

Frequency

Phase Unbalance

Amplitude Unbalance

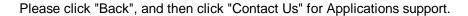
Insertion Loss Above 3.0 dB

Isolation

VSWR

Important Note

This model has been designed, built and tested in our engineering department. Performance data represents model capability. At present it is a non-catalog model. On request, we can supply a final specification sheet, part number and price/delivery information.



1500-18000 MHz

1500-18000 MHz

1500-18000 MHz

1500-18000 MHz

SUM Port

OUT Ports

ELECTRICAL SPECIFICATIONS

Min.

1500



CASE STYLE: KB1450

C		
	Max.	Units
	18000	MHz
		dB
		dB
		deg.
		dB
		(:1)

(:1)

MAXIMUM RATINGS		
Operating Temperature	-55°C to 100°C	
Storage Temperature	-55°C to 100°C	

COAXIALCONNECTIONS			
SUM PORT	S		
PORT 1	1		
PORT 2	2		

electrical schematic

50Ω @ +25°C

Тур.

20

1.00

2.90

0.22

1.55

1.50





