## THE BIG DEAL

- 2.4 to 2.5 GHz ISM band
- 30 dB attenuation range
- $360^{\circ}$ phase shift range
- Suitable for CW and pulsed signals
- Easy integration with ZHL-2425-250X+ and ISC-2425-25+
- I2C control interface
- Isolated Paths


## APPLICATIONS

- RF energy generators
- Industrial heating
- Plasma generators
- S-band high-power amplifiers


Generic photo used for illustration purposes only
FUNCTIONAL DIAGRAM


## PRODUCT OVERVIEW

The SPL-2G42G50W4+ is a 4-way RF splitter for the $2.4-2.5 \mathrm{GHz}$ ISM band. The splitter is capable of more than $360^{\circ}$ of phase shift with $1^{\circ}$ tuning resolution. Each path also has an amplitude tuning range of 30 dB with a step size of 0.5 dB . The total phase shift of the unit is accomplished with a $160^{\circ}$ continuously variable phase shifter coupled with a $180^{\circ}$ phase inversion bit. This unit can be used to easily construct a 1KW RF energy system built with 4x ZHL-2425-250X+, the ISC-2425-25+ and the COM2G42G51K0+.

## KEY FEATURES

| Features |  |
| :--- | :--- |
| Gain Variation | 30 dB of variable Gain with 0.5 dB step size, individually controlled per channel. |
| Phase Variation | $360^{\circ}$ phase shift, $160^{\circ}$ with $1^{\circ}$ resolution and a $180^{\circ}$ phase inversion bit |
| I2C bus | I2C bus for control of the phase and amplitude. The bus is switchable and provides 4 <br> amplifiers ond one peripheral component such as a power supply |
| Easy interfacing for controlling 4 |  |

ELECTRICAL SPECIFICATIONS AT $\mathrm{T}_{\text {BASE }}=+25^{\circ} \mathrm{C} \mathrm{V}_{\text {DS }}=5 \mathrm{~V}$

| Parameter | Frequency (MHz) | Min. | Typ. | Max. | Units |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Frequency Range | - | 2400 | - | 2500 | MHz |
| Insertion Loss ${ }^{1,2}$ | 2400-2500 | - | 1 | 2 | dB |
| Continuous Phase Adjustment ${ }^{3}$ | 2400-2500 | 155 | 165 | - | Degree |
| Phase Resolution | 2400-2500 | - | 1 | - | Degree |
| 180 degree Phase BIT ${ }^{1,3}$ | 2400-2500 | 165 | 180 | - | Degree |
| Amplitude Adjustment ${ }^{4}$ | 2400-2500 | 30 | - | - | dB |
| Amp Resolution | 2400-2500 | - | 0.5 | - | Degree |
| VSWR (RF_IN) | 2400-2500 | - | - | 2.3 | :1 |
| VSWR (RF_OUT) | 2400-2500 | - | - | 2.0 | :1 |
| Supply Current | 2400-2500 | - | 650 | - | mA |

1. All ports must be terminated with $50 \Omega$
2. Across all DAC values
3. AMPLITUDE DAC $=0$
4. $\mathrm{PHASE} D A C=0$

## ABSOLUTE MAXIMUM RATINGS ${ }^{5}$

| Parameter | Ratings |
| :--- | :---: |
| Operating Base Temperature ${ }^{5}$ | $0^{\circ} \mathrm{C}$ to $85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-55^{\circ} \mathrm{C}$ to $100^{\circ} \mathrm{C}$ |
| Power Input $^{6}$ | +18 dBm |
| Voltage | 5.5 V |

5. Permanent damage may occur if any of these limits are exceeded.
6. 10 minutes test time at 2500 MHz

## APPLICATION OVERVIEW

The SPL-2G42G504W+ was designed to be integrated with the ZHL-2425-250X+, the ISC-2425-25+, and the COM2G42G51K0+. The SPL-2G42G504W+ is designed to enable matching of the phase and amplitude of the four independent paths of the system. The cables, ZHL-2425-250X+ and the COM-2G42G51KO+ all have their individual phase and amplitude differences which can be overcome by the adjustment of the channels in the SPL-2G42G504W+. The bidirectional control from each ZHL-2425-250X+ is input on each of the four CTRL_PA [1 ..4] inputs and are routed through to the CTRL_MASTER which interfaces with the ISC-2425-25+. There is a control signal that routes the I2C communication to an external component, in many instances this is a power supply feeding the $4 \times \mathrm{ZHL}-2425-250 \mathrm{X}+$.

Every ZHL-2425-250X+ comes preprogrammed from the factory with the same address. To access the individual ZHL-2425$250 X+$ amplifiers, they all need to have their own unique address. The SPL-2G42G50W5+ has an in-line I2C switch that allows the selection of individual PA channels, so they can be programmed with an unique address. This programming function is available through the CTRL_MASTER port which connects to the CTRL_PA[1..4] ports iteratively. This function is most useful at initial system configuration.

Once the addresses have been configured the ZHL PA's will have their new address after the next $\mathrm{V}_{\mathrm{DD}}$ power cycle. It is important to note that the I2C switch has all I2C paths open on startup and an enable command has to be sent to choose individual channels or turn all channels on $0 \times 700 \mathrm{Fh}$.

## I2C BUS

The SPL-2G42G50W4+ has six I2C interfaces, there are 3 different types that the i2C bus resides on. The first type of interface is the main interface CTRL MASTER, this interface controls a switch that routes the I2C bus to the four external PA control connectors CTRL_PA[1..4]. The 2nd type of interface is replicated four times to interface to external PAs such as the ZHL-$2425-250 X+$. The third interface simply routes the I2C bus directly from the CTRL_MASTER. The pinout and function of these interfaces is described in the tables below.

CONTROL INTERFACE PIN-OUT AND FUNCTIONALITY FOR CONNECTORS CTRL_MASTER, CTRL_PA1, CTRL_PA2, CTRL_PA3, CTRL_PA4

| Pin Number | Label | Fupe |  |
| :---: | :---: | :--- | :--- |
| 1 | PIN1 |  | Connected through to Pin 1 CTRL PA1, PA2, PA3, PA4 |
| 2 | PIN2 |  | Connected through to Pin 2 CTRL PA1, PA2, PA3, PA4 |
| 3 | PIN3 |  | Connected through to Pin 3 CTRL PA1, PA2, PA3, PA4 |
| 4 | PIN4 |  | Connected through to Pin 5 CTRL PA1, PA2, PA3, PA4 |
| 5 | PIN5 |  | Connected through to Pin 6 CTRL PA1, PA2, PA3, PA4 |
| 6 | PIN6 |  | Conerved pin for manufacturer |
| 7 | Do Not Connect |  | Ground |
| 8 | PIN8 |  | Ground |
| 10 | PIN9 |  | I2C control |
| 11 | GND |  | Connected through to Pin 8 CTRL PA1, PA2, PA3, PA4 |
| 12 | Go Not Connect |  | I2C control |
| 13 | SCL |  | Ground |
| 14 | PIN14 |  |  |
| 15 | SDA |  |  |
| 16 | GND |  |  |
| $17-20$ | Do Not Connect |  |  |

(CTRL_PS) CONTROL INTERFACE PIN-OUT AND FUNCTIONALITY

| Pin Number | Label | Type |  |
| :---: | :---: | :---: | :--- |
| $1-9,11,14,17-20$ | No connect |  |  |
| $10,12,16$ | GND |  | Ground |
| 13 | SCL |  | I2C control |
| 15 | SDA |  | I2C control |

REGISTER TABLE

| Register \# | Function | Range |
| :---: | :---: | :---: |
| 0x6008h | Set Attenuation, Channel 1 | 0-63, 0.5 dB per LSB |
| 0x6028h | Set Attenuation, Channel 2 | 0-63, 0.5 dB per LSB |
| 0x6030h | Set Attenuation, Channel 3 | 0-63, 0.5 dB per LSB |
| 0x6010h | Set Attenuation, Channel 4 | $0-63,0.5 \mathrm{~dB}$ per LSB |
| 0x6018h | Set Phase, Channel 1 | $0-255$ controls the variable phase shift in $\sim 1^{\circ}$ steps per LSB. Setting 256 flips the $180^{\circ}$ bit and restarts the variable phase shifter. Full range of this command is 0-511 |
| 0x6038h | Set Phase, Channel 2 | $0-255$ controls the variable phase shift in $\sim 1^{\circ}$ steps per LSB. Setting 256 flips the $180^{\circ}$ bit and restarts the variable phase shifter. Full range of this command is 0-511 |
| 0x6020h | Set Phase, Channel 3 | $0-255$ controls the variable phase shift in $\sim 1^{\circ}$ steps per LSB. Setting 256 flips the $180^{\circ}$ bit and restarts the variable phase shifter. Full range of this command is 0-511 |
| 0x6000h | Set Phase, Channel 4 | $0-255$ controls the variable phase shift in $\sim 1^{\circ}$ steps per LSB. Setting 256 flips the $180^{\circ}$ bit and restarts the variable phase shifter. Full range of this command is 0-511 |
| 0x600Eh | Read Attenuation, Channel 1 | [MSB,LSB] |
| 0x602Eh | Read Attenuation, Channel 2 | [MSB,LSB] |
| 0x6036h | Read Attenuation, Channel 3 | [MSB,LSB] |
| 0x6016h | Read Attenuation, Channel 4 | [MSB,LSB] |
| 0x601Eh | Read Phase, Channel 1 | [MSB,LSB] |
| 0x603Eh | Read Phase, Channel 2 | [MSB,LSB] |
| 0x6026h | Read Phase, Channel 3 | [MSB,LSB] |
| 0x6006h | Read Phase, Channel 4 | [MSB,LSB] |
| 0x7001h | Enable I2C, Channel 1 Only | Enable the control PA1 path |
| 0x7002h | Enable I2C, Channel 2 Only | Enable the control PA2 path |
| 0x7003h | Enable I2C, Channel 3 Only | Enable the control PA3 path |
| 0x7004h | Enable I2C, Channel 4 Only | Enable the control PA4 path |
| 0x700Fh | Enable all I2C, Channels | Enables all I2C paths |
| 0x6070Fh | Assert (Enable) RFEN |  |
| 0x6078Fh | De-assert (Disable) RFEN |  |

## Active Power Splitter

TYPICAL PERFORMANCE DATA ACROSS FREQUENCY AT DIFFERENT DAC VALUES
( $\mathrm{T}_{\text {BASE }}=25^{\circ} \mathrm{C}, 50 \Omega$ SYSTEM, $\mathrm{V}_{\mathrm{DS}}=5 \mathrm{~V}$ TYP., $\mathrm{I}_{\mathrm{DC}}=650 \mathrm{~mA}$ TYP. )




# Active Power Splitter <br> SPL-2G42G50W4+ 

$\left(T_{\text {BASE }}=0^{\circ} \mathrm{C}, 25^{\circ} \mathrm{C}, 85^{\circ} \mathrm{C}, 50 \Omega\right.$ SYSTEM, $\mathrm{V}_{\mathrm{DS}}=5 \mathrm{~V}$ TYP., $\mathrm{I}_{\mathrm{DC}}=650 \mathrm{~mA}$ TYP.


Attentuation level S21 vs. Frequency


## Active Power Splitter spl-2Gazasowat

TYPICAL PERFORMANCE DATA ACROSS TEMPERTATURE ( $50 \Omega$ SYSTEM, $V_{D s}=5$ V TYP., $I_{D C}=650 \mathrm{~mA}$ TYP. )

Max Phase Change vs. Temperature at 2.45 GHz


TYPICAL PERFORMANCE DATA ACROSS FREQUENCY ( $\mathrm{T}_{\text {BASE }}=25^{\circ} \mathrm{C}, 50 \Omega$ SYSTEM, $\mathrm{V}_{\mathrm{DS}}=5 \mathrm{~V}$ TYP., $\mathrm{I}_{\mathrm{DC}}=650 \mathrm{~mA}$ TYP. )


| Input / Output | MCX -Female |
| :---: | :---: |

## CASE STYLE DRAWING



Weight: 220 grams
Dimensions are in inches [mm]. Tolerances: 2 Pl. $\pm 0.03$ inch; 3 Pl. $\pm 0.015$ inch
Recommended screws for mounting model : Use M3 button head

## ADDITIONAL INFORMATION IS AVAILABLE ON OUR DASHBOARD

| Performance Data \& Graphs | Data |
| :--- | :--- |
| Graphs |  |
| Case Style | VU3558 |
| RoHS Status | Compliant |
| Environmental Ratings | ENV28T22 |

ORDERING INFORMATION

| Model No. Links | SPL-2G42G50W4+ |
| :--- | :--- |
| Product Marking | SPL-2G4250W4+ |
| Case Style | VU3558 |
| Connectors | MCX-Female |

## SAFETY INSTRUCTIONS.

WARNING: FAILURE TO FOLLOW THESE INSTRUCTIONS MAY RESULT IN BODILY INJURY, DEATH, OR PROPERTY DAMAGE.
For your own safety, this section provides instructions for avoiding potential dangers when using this product.
QUALIFIED PERSONNEL
This product should be operated by qualified personnel only. Qualified personnel are individuals who are familiar with the operation of the product and the hazards involved with such operation.

DAMAGED OR MISSING HARDWARE
Do not operate the product if there is physical damage or hardware is missing.
MAXIMUM RATINGS
The maximum ratings in this data sheet should never be exceeded. Stress above one or more maximum ratings may cause permanent damage to the product and may permanently and irreversibly affect the quality and reliability of the product, which may increase the risk of bodily injury, death, or property damage.

## HAZARDOUS RF VOLTAGES

The RF voltages inside the product and on the center pin of the RF output connector can be hazardous. Contact with the internal components of the product or the center pin of the RF output connector may lead to burns or electrical shock. Disconnect power before removing the protective cover from the product. Note that removing the protective cover from the product will void the express warranty specified in MiniCircuits Standard Terms.

To reduce the risks presented by these hazards:

1. never operate the product without its protective cover,
2. always connect the RF output connector to a load before the power source is applied to the product, and
3. always place the product in a non-operating condition before disconnecting or connecting the load to the RF output connector.

COOLING
RF Power amplifiers always need proper cooling. Failure to properly cool the product may increase the risk of bodily injury, death, or damage to property or the product.

Some products contain water cooling systems to help cool down the product. If this data sheet indicates that the product contains a water cooling system, proper waterflow as specified in this data sheet is required to keep the temperature of the product within the temperature range that is specified in this data sheet.

Some products also contain built-in protection circuitry designed to shut-off the amplifier at excessive high temperatures or at other excessive operating conditions. Even if this data sheet indicates that the product contains protective circuitry, such protective circuitry is not a substitute for proper handling in accordance with these instructions. Accordingly, do not rely on the protective circuitry to prevent injury or damage to property or the product.

## MAINTENANCE CAUTION

Maintenance or repair of the product must only be performed by qualified personnel when the product is in a non-operating condition and disconnected from its power source. Note that performance of maintenance or repairs to the product will void the express warranty specified in Mini-Circuits Standard Terms.

## ENVIRONMENTAL CONDITIONS

Unless otherwise stated in this data sheet, this product is designed to be operated under the environmental conditions set forth in this data sheet, as well as the following conditions:

- Indoor use only
- Temperature of $5^{\circ} \mathrm{C}$ to $40^{\circ} \mathrm{C}$ (non-condensing)


## WARNING SIGNS

In addition to being qualified before operating the product, pay attention to all warning signs and danger symbols. Failure to heed warnings signs and danger symbols, or to follow their associated instructions, may result in bodily injury, death, or property damage.

## Typical Performance Data

TEST CONDITION: $T_{\text {BASE }}=25^{\circ} \mathrm{C}, 50 \Omega$ SYSTEM, $\mathrm{V}_{\mathrm{DS}}=5 \mathrm{~V}$ typ., $\mathrm{ID}_{\mathrm{C}}=650 \mathrm{~mA}$ typ.

| Freqency | S11 |  |  | S21 |  |  | S22 |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (GHz) | (dB) |  |  | (dB) |  |  | (dB) |  |  |
|  | DAC Value "0" | $\begin{array}{\|c\|} \hline \text { DAC } \\ \text { Value "31" } \\ \hline \end{array}$ | DAC <br> Value "63" | $\begin{array}{\|c} \hline \text { DAC } \\ \text { Value "0" } \end{array}$ | DAC Value "31" | DAC <br> Value "63" | $\begin{array}{\|c\|} \hline \text { DAC } \\ \text { Value " } 0 \text { " } \\ \hline \end{array}$ | $\begin{array}{\|c\|} \hline \text { DAC } \\ \text { Value "31" } \end{array}$ | DAC <br> Value "63" |
| 2.40 | -11.07 | -11.29 | -11.42 | -0.13 | -16.14 | -33.04 | -13.22 | -13.40 | -13.40 |
| 2.41 | -11.90 | -12.11 | -12.26 | -0.05 | -16.09 | -32.99 | -13.53 | -13.74 | -13.74 |
| 2.42 | -12.69 | -12.93 | -13.09 | 0.02 | -16.06 | -32.93 | -13.89 | -14.12 | -14.11 |
| 2.43 | -13.29 | -13.59 | -13.77 | 0.08 | -16.03 | -32.95 | -14.29 | -14.50 | -14.50 |
| 2.44 | -13.49 | -13.90 | -14.08 | 0.12 | -16.05 | -32.93 | -14.72 | -14.90 | -14.89 |
| 2.45 | -13.28 | -13.79 | -13.96 | 0.12 | -16.09 | -32.97 | -15.17 | -15.29 | -15.28 |
| 2.46 | -12.72 | -13.33 | -13.48 | 0.11 | -16.16 | -33.06 | -15.58 | -15.64 | -15.63 |
| 2.47 | -12.02 | -12.68 | -12.81 | 0.08 | -16.22 | -33.15 | -15.97 | -15.94 | -15.93 |
| 2.48 | -11.29 | -11.98 | -12.10 | 0.04 | -16.30 | -33.23 | -16.31 | -16.19 | -16.17 |
| 2.49 | -10.64 | -11.33 | -11.45 | -0.03 | -16.39 | -33.28 | -16.54 | -16.36 | -16.34 |
| 2.50 | -10.07 | -10.77 | -10.87 | -0.10 | -16.47 | -33.36 | -16.67 | -16.44 | -16.42 |

TEST CONDITION: DAC VALUE $=0,50 \Omega$ SYSTEM, $V_{D S}=5 \mathrm{~V}$ typ., $I_{C}=650 \mathrm{~mA}$ typ.

| Freqency | S11 |  |  | S21 |  |  | S22 |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| (GHz) | (dB) |  |  | (dB) |  |  | (dB) |  |  |
|  | $0^{\circ} \mathrm{C}$ | $25^{\circ} \mathrm{C}$ | $85^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $25^{\circ} \mathrm{C}$ | $85^{\circ} \mathrm{C}$ | $0^{\circ} \mathrm{C}$ | $25^{\circ} \mathrm{C}$ | $85^{\circ} \mathrm{C}$ |
| 2.40 | -9.23 | -11.07 | -11.29 | -0.13 | -0.29 | -1.20 | -13.25 | -13.22 | -12.86 |
| 2.41 | -9.92 | -11.90 | -11.99 | -0.05 | -0.23 | -1.16 | -13.52 | -13.53 | -13.17 |
| 2.42 | -10.75 | -12.69 | -12.62 | 0.02 | -0.19 | -1.14 | -13.86 | -13.89 | -13.49 |
| 2.43 | -11.68 | -13.29 | -13.11 | 0.08 | -0.17 | -1.15 | -14.23 | -14.29 | -13.80 |
| 2.44 | -12.62 | -13.49 | -13.36 | 0.12 | -0.18 | -1.16 | -14.67 | -14.72 | -14.10 |
| 2.45 | -13.44 | -13.28 | -13.35 | 0.12 | -0.23 | -1.21 | -15.14 | -15.17 | -14.37 |
| 2.46 | -13.95 | -12.72 | -13.10 | 0.11 | -0.29 | -1.26 | -15.61 | -15.58 | -14.58 |
| 2.47 | -14.00 | -12.02 | -12.71 | 0.08 | -0.37 | -1.32 | -16.07 | -15.97 | -14.74 |
| 2.48 | -13.63 | -11.29 | -12.24 | 0.04 | -0.44 | -1.38 | -16.49 | -16.31 | -14.83 |
| 2.49 | -13.01 | -10.64 | -11.77 | -0.03 | -0.54 | -1.45 | -16.85 | -16.54 | -14.86 |
| 2.50 | -12.30 | -10.07 | -11.30 | -0.10 | -0.62 | -1.51 | -17.10 | -16.67 | -14.83 |

TEST CONDITION:DAC Value 255 Frequency $2.45 \mathrm{GHz} 25^{\circ} \mathrm{C}, 50 \Omega$ SYSTEM, VDS=5 V typ., IDC =650 mA typ.

| Temperature | Phase <br> Change |
| :---: | :---: |
| ${ }^{\circ} \mathrm{C}$ | Degrees |
| 0 | -179.288 |
| 25 | -179.486 |
| 85 | -181.789 |

TEST CONDITION: DAC Value 256 Frequency $2.45 \mathrm{GHz} 25^{\circ} \mathrm{C}, 50 \Omega$ SYSTEM, VDS=5 V typ., IDC $=650 \mathrm{~mA}$ typ.

| Freqency | Phae Invesrion |
| :---: | ---: |
| $\mathbf{( G H z )}$ | Degrees |
|  |  |
| 2.40 | -175.066 |
| 2.41 | -175.428 |
| 2.42 | -175.766 |
| 2.43 | -176.078 |
| 2.44 | -176.382 |
| 2.45 | -176.651 |
| 2.46 | -176.877 |
| 2.47 | -177.07 |
| 2.48 | -177.24 |
| 2.49 | -177.323 |
| 2.50 | -177.406 |

## Typical Performance Curves

CONDITION: AT DIFFERENT DAC VALUES ( $\mathrm{T}_{\mathrm{BASE}}=25^{\circ} \mathrm{C}, 50 \Omega$ SYSTEM, $\mathrm{V}_{\mathrm{DS}}=5 \mathrm{~V}$ typ., $\mathrm{I}_{\mathrm{DC}}=650 \mathrm{~mA}$ typ. $)$


## Typical Performance Curves

CONDITION: AT DAC VALUE $0\left(T_{\text {BASE }}=0^{\circ} \mathrm{C}, 25^{\circ} \mathrm{C}, 85^{\circ} \mathrm{C}, 50 \Omega\right.$ SYSTEM, $\mathrm{V}_{\mathrm{DS}}=5 \mathrm{~V}$ typ., $\mathrm{I}_{\mathrm{DC}}=650 \mathrm{~mA}$ typ. $)$



## Typical Performance Curves

CONDITION: ACROSS TEMPERTATURE IN $50 \Omega$ SYSTEM, $\mathrm{V}_{\mathrm{DS}}=5 \mathrm{~V}$ typ., $\mathrm{I}_{\mathrm{DC}}=650 \mathrm{~mA}$ typ.


CONDITION: $\mathrm{T}_{\text {BASE }}=25^{\circ} \mathrm{C}, 50 \Omega$ SYSTEM, $\mathrm{V}_{\mathrm{DS}}=5 \mathrm{~V}$ typ., $\mathrm{I}_{\mathrm{DC}}=650 \mathrm{~mA}$ typ.


## Case Style



Dimensions are in inches [mm]. Tolerances: $2 \mathrm{PI} . \pm 0.03$ inch; $3 \mathrm{Pl} . \pm 0.015$ inch

Notes:

1. Case Material: Aluminum
2. Case Finish: Polished Aluminum
3. Recommended screws for mounting model : Use M3 button head.
4. Weight: 220 grams

## $\square$ Mini-Circuits

All Mini-Circuits products are manufactured under exacting quality assurance and control standards, and are capable of meeting published specifications after being subjected to any or all of the following physical and environmental test


