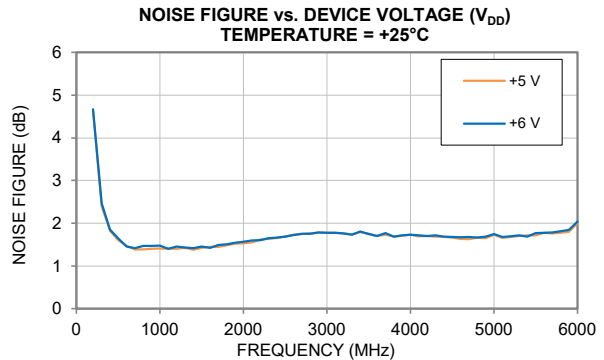
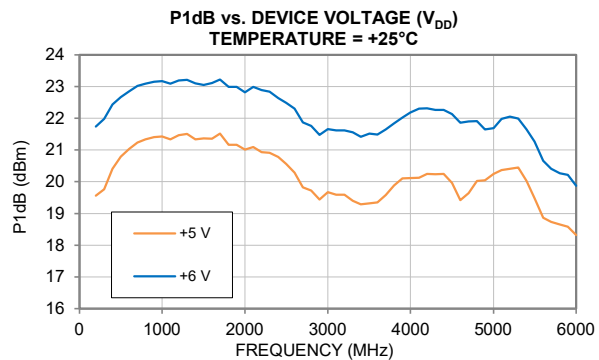
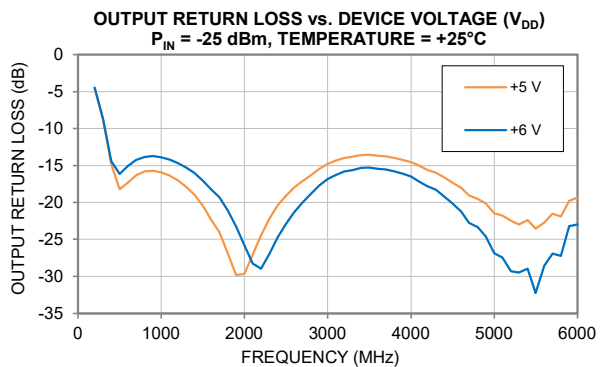
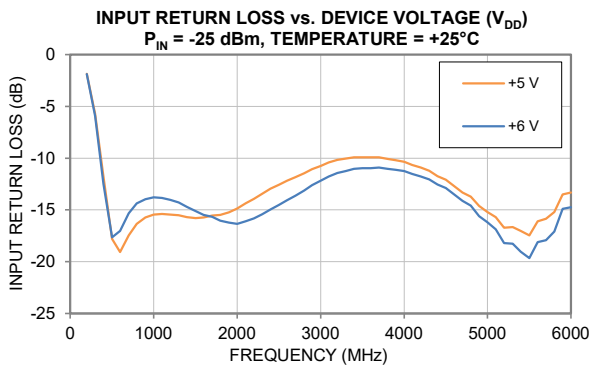
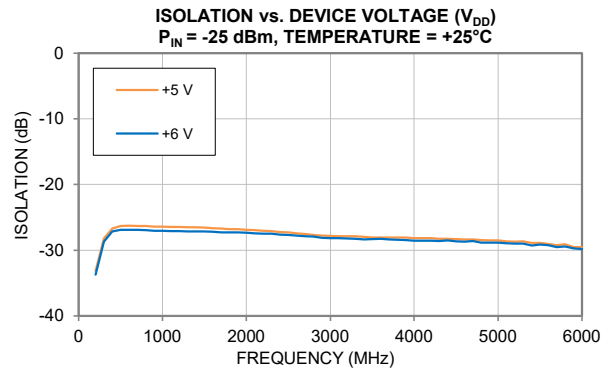
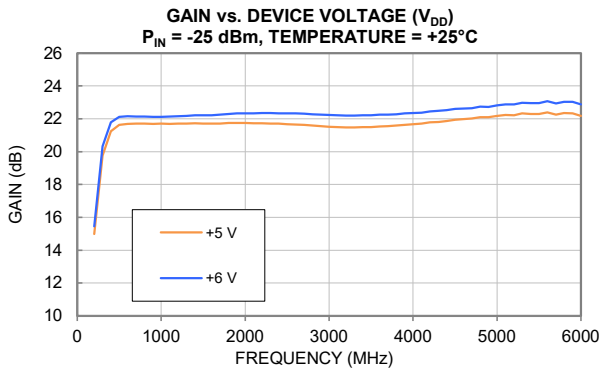


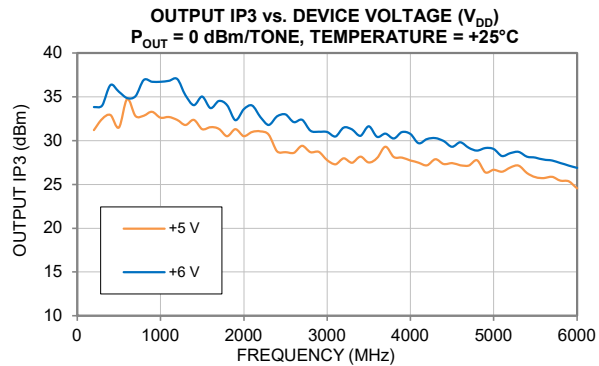
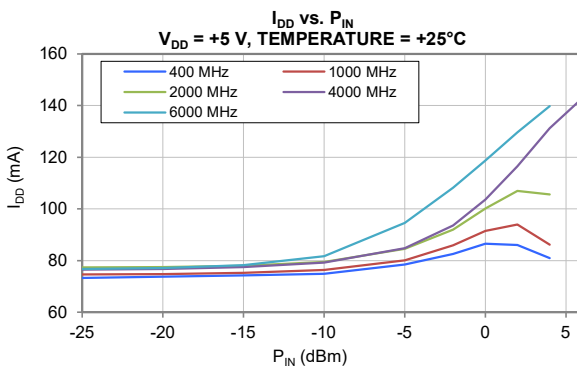
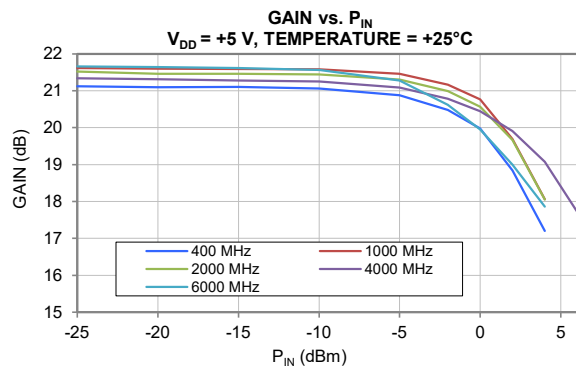
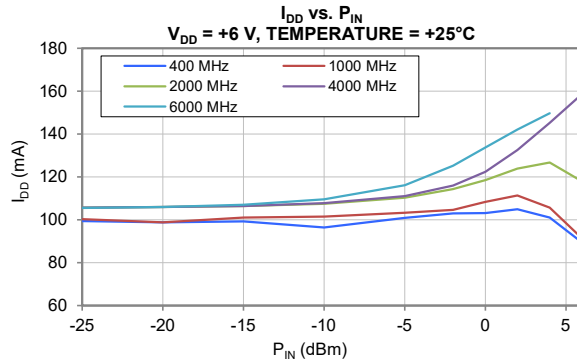
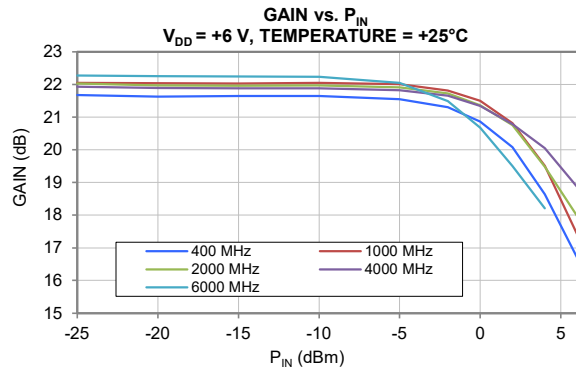
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-83LNC+ (Figure 2). All data taken at nominal conditions $V_{EN} = V_{DD}$ and $R_{I,ADJ} = \text{Open}$ unless noted otherwise.



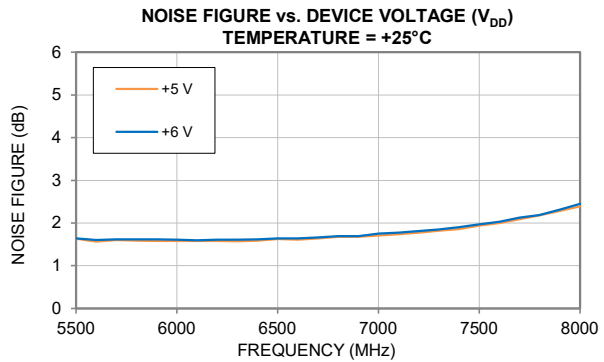
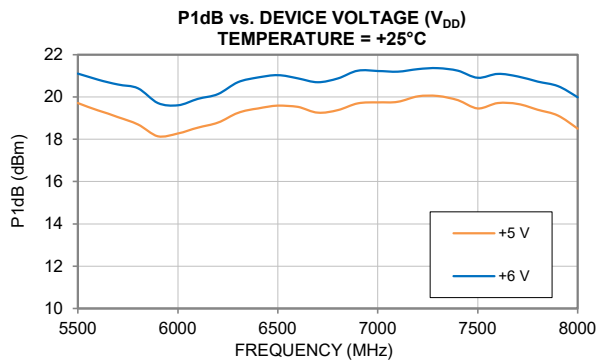
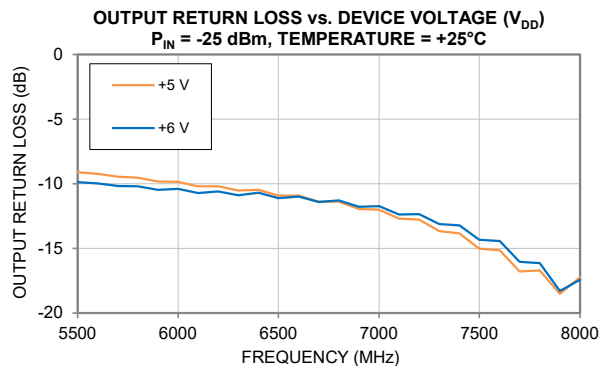
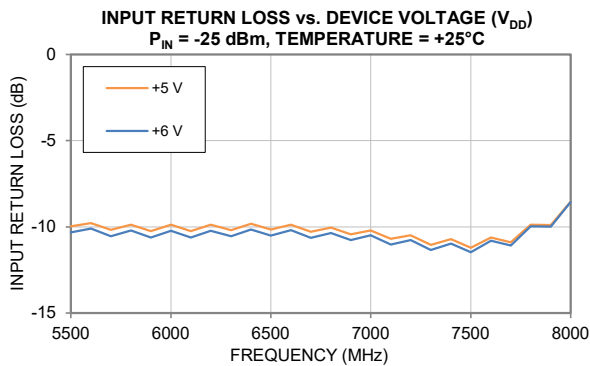
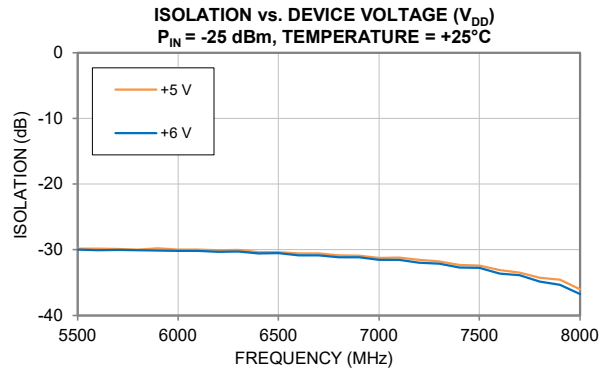
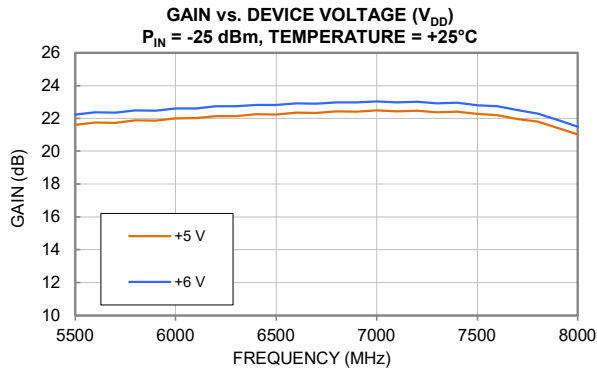
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-83LNC+ (Figure 2). All data taken at nominal conditions $V_{EN} = V_{DD}$ and $R_{LADJ} = \text{Open}$ unless noted otherwise.



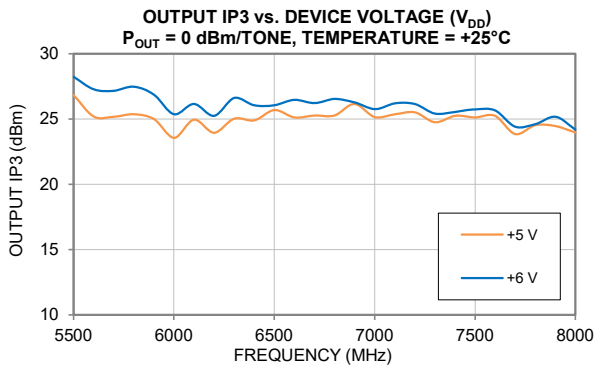
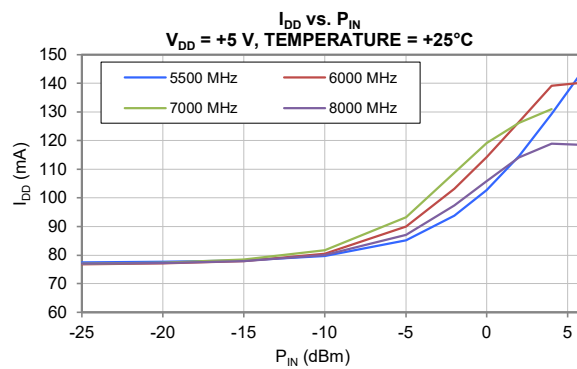
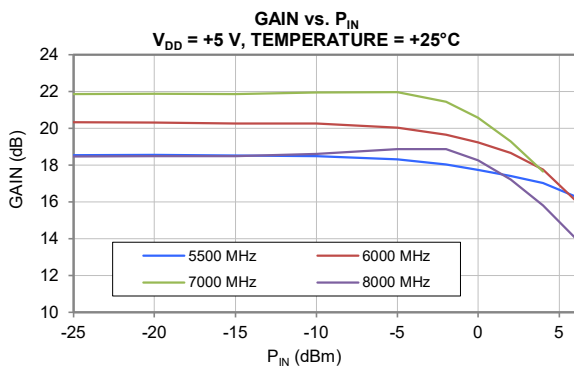
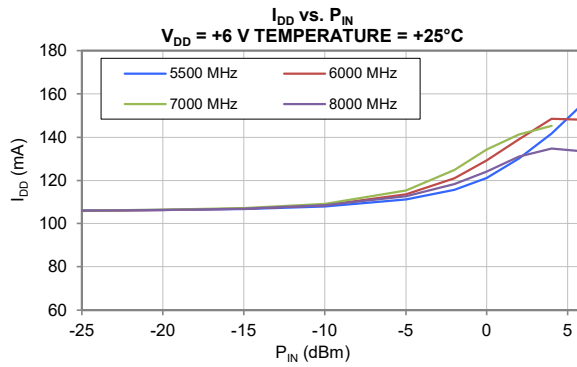
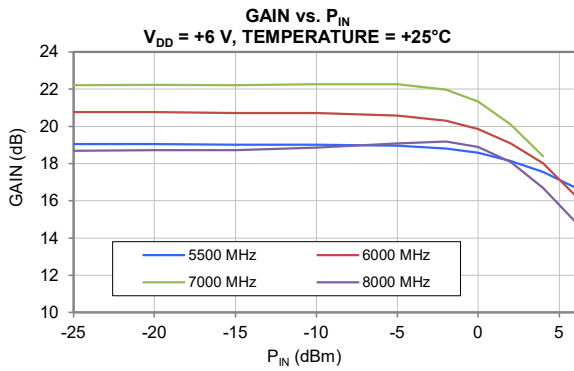
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-832LNC+ (Figure 3). All data taken at nominal conditions $V_{EN} = V_{DD}$ and $R_{LADJ} = \text{Open}$ unless noted otherwise.



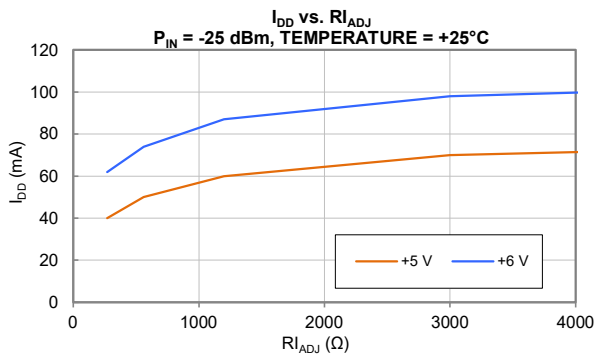
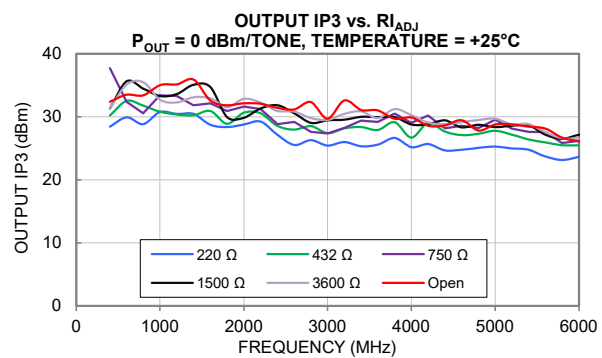
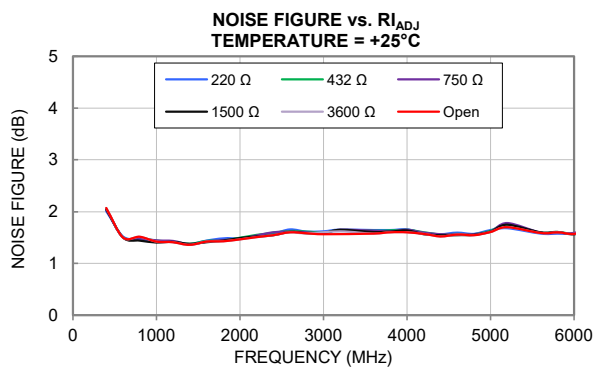
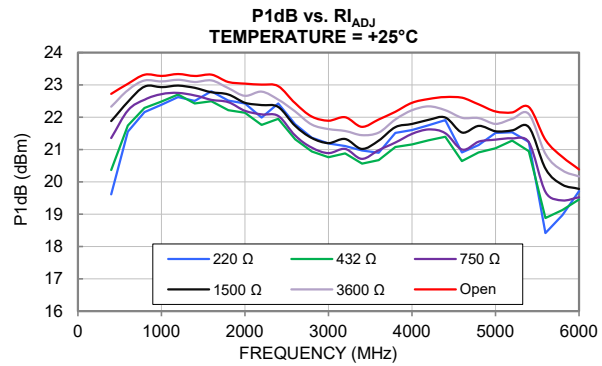
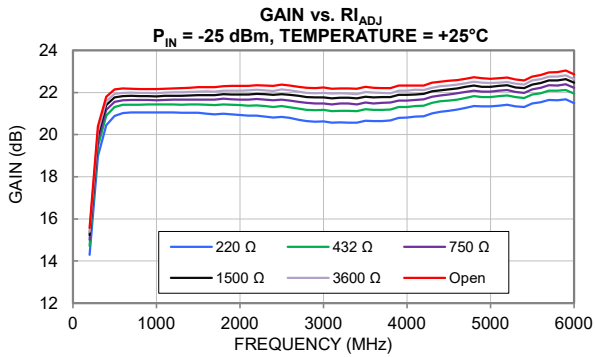
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-832LNC+ (Figure 3). All data taken at nominal conditions $V_{EN} = V_{DD}$ and $R_{I_{ADJ}} = \text{Open}$ unless noted otherwise.



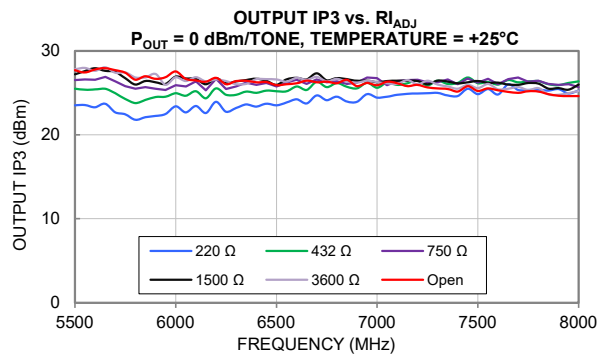
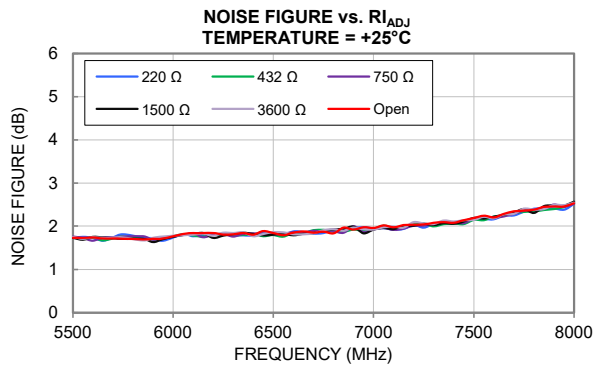
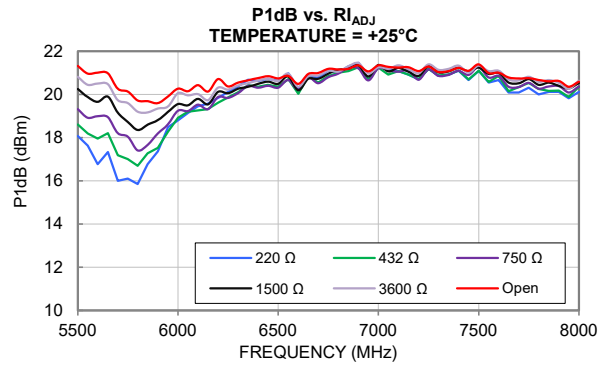
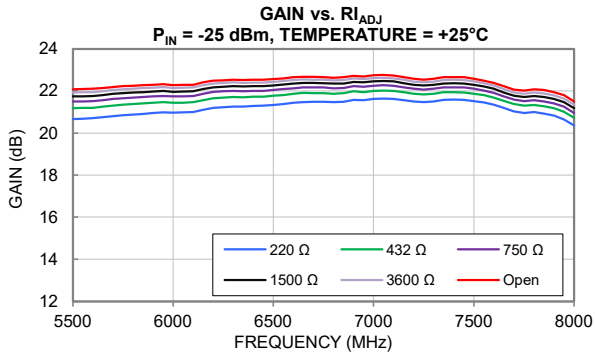
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-83LNC+ (Figure 2). All data taken at nominal conditions $V_{EN} = V_{DD} = +6 V$ unless noted otherwise.



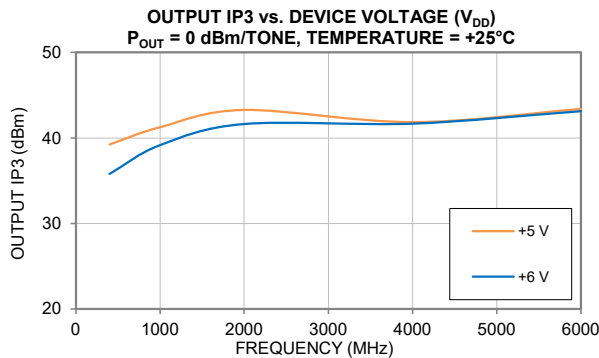
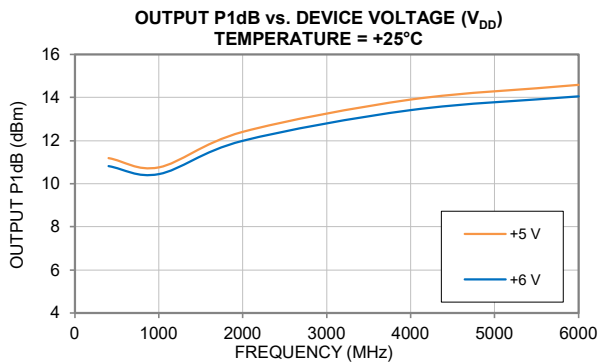
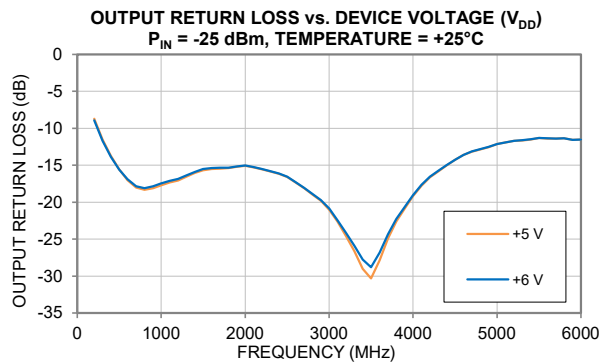
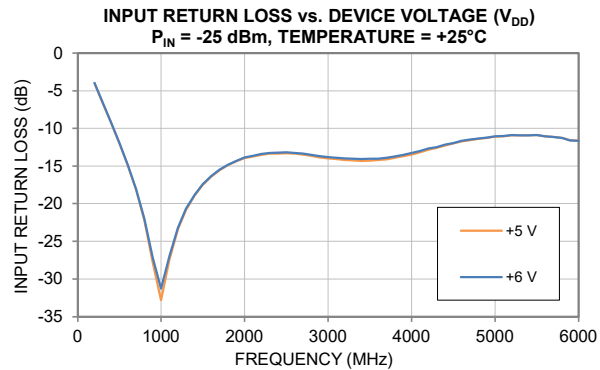
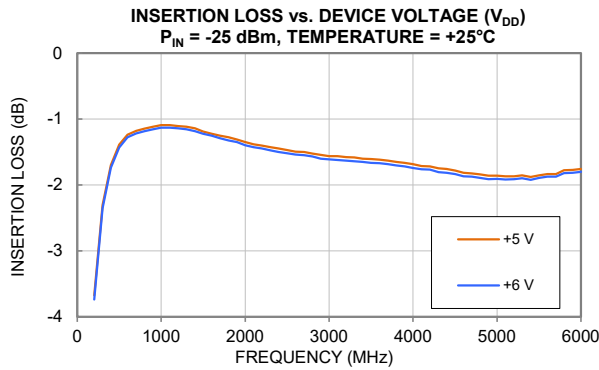
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-832LNC+ (Figure 3). All data taken at nominal conditions $V_{EN} = V_{DD} = +6\text{ V}$ unless noted otherwise.



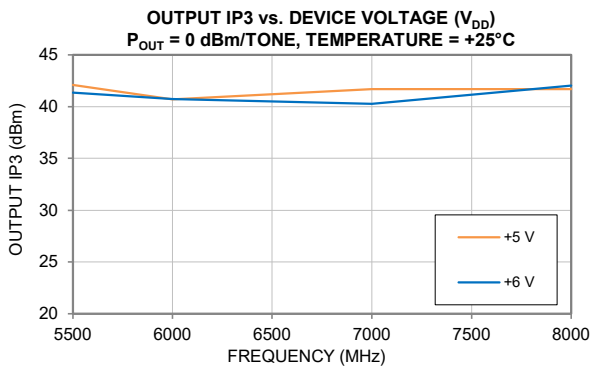
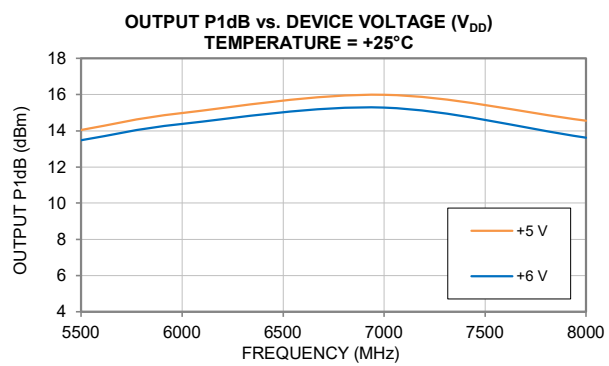
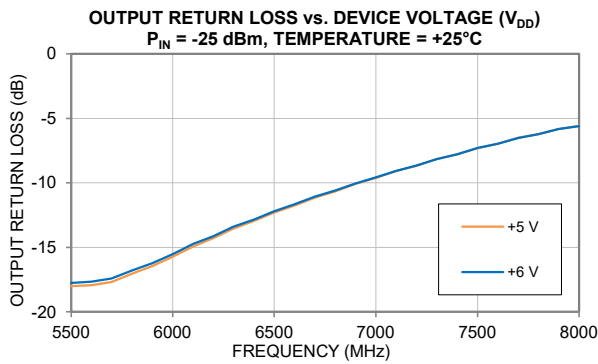
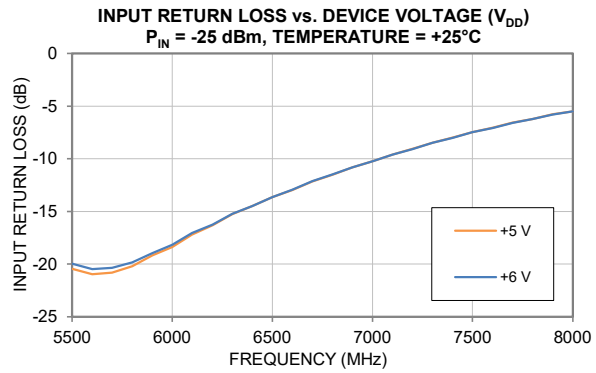
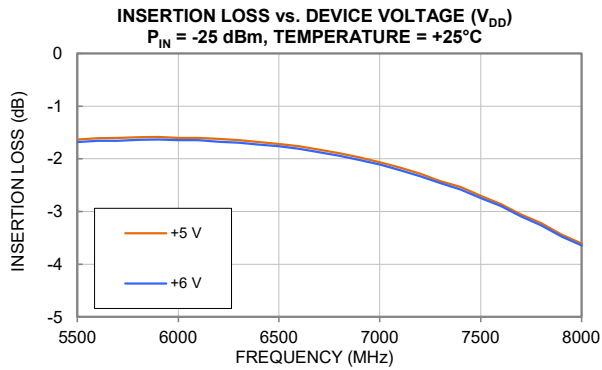
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-83LNC+ (Figure 2). All data taken at nominal conditions $V_{EN} = 0$ V and $R_{LADJ} = \text{Open}$ unless noted otherwise.



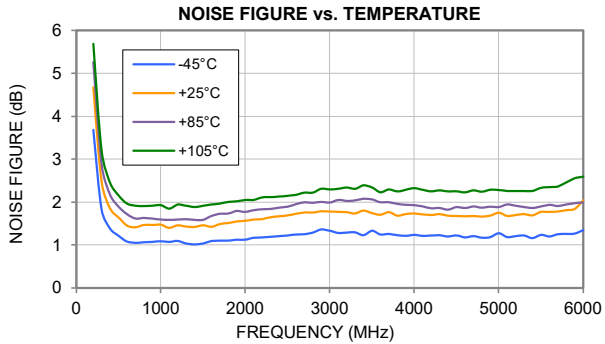
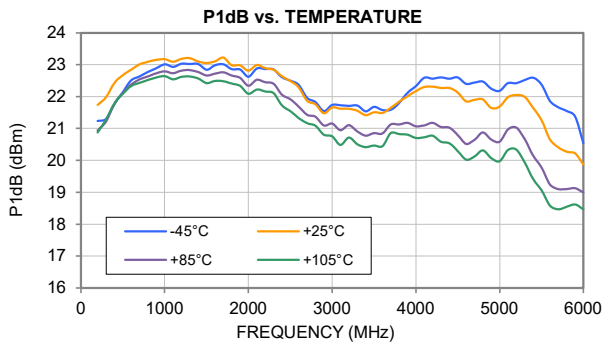
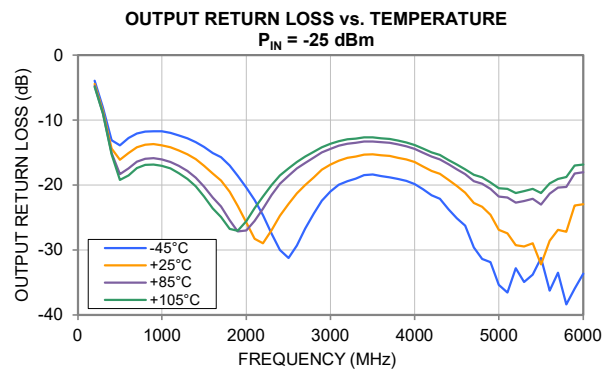
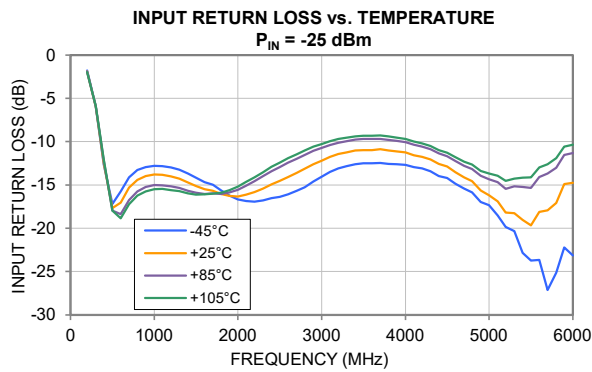
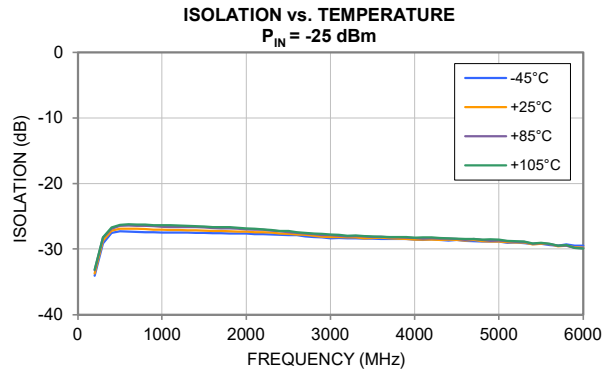
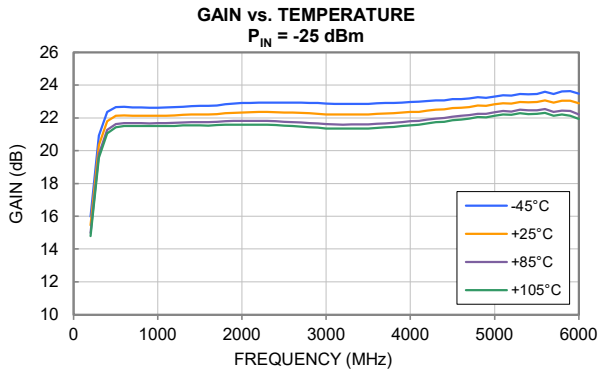
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-832LNC+ (Figure 3). All data taken at nominal conditions $V_{EN} = 0$ V and $R_{LADJ} = \text{Open}$ unless noted otherwise.



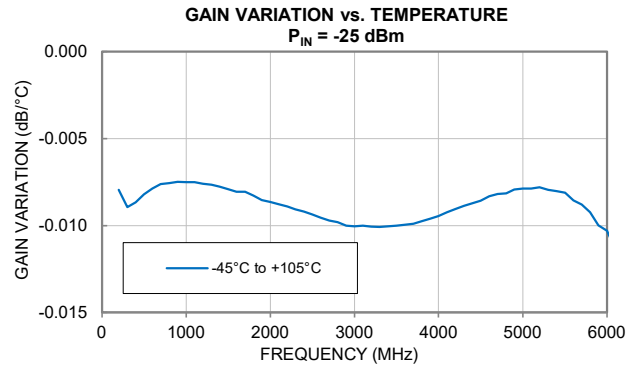
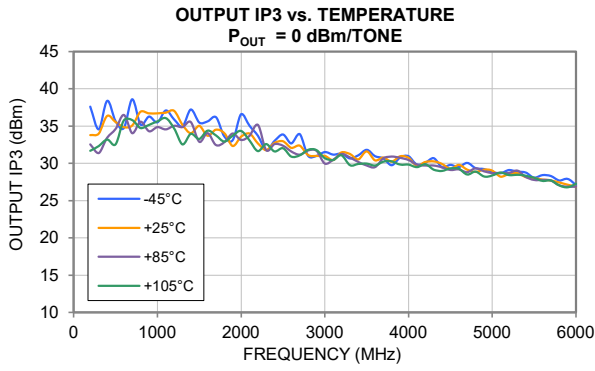
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-83LNC+ (Figure 2). All data taken at nominal conditions $V_{EN} = V_{DD} = +6\text{ V}$, and $R_{ADJ} = \text{Open}$ unless noted otherwise.



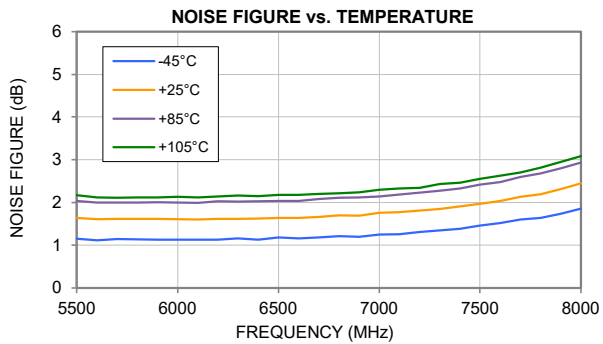
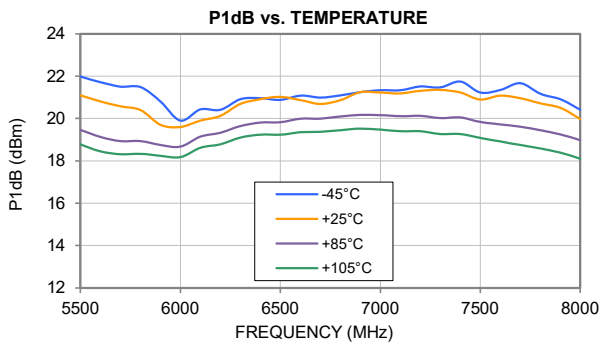
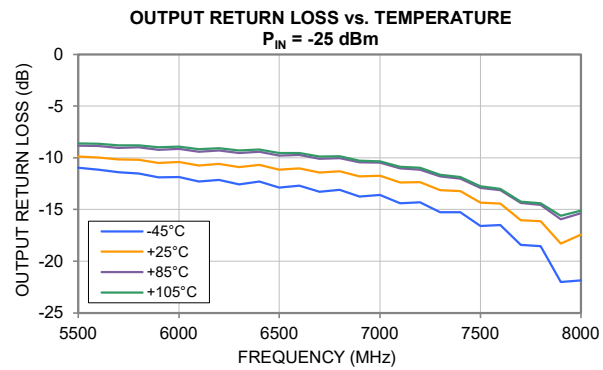
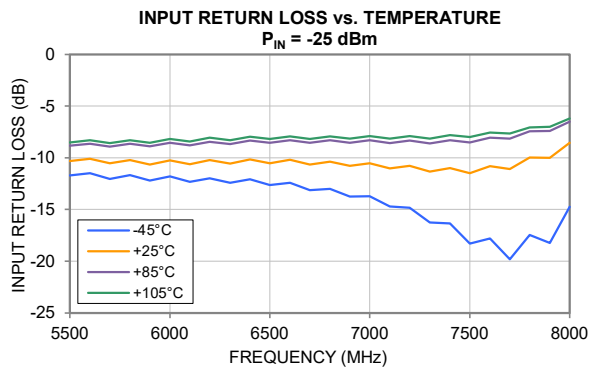
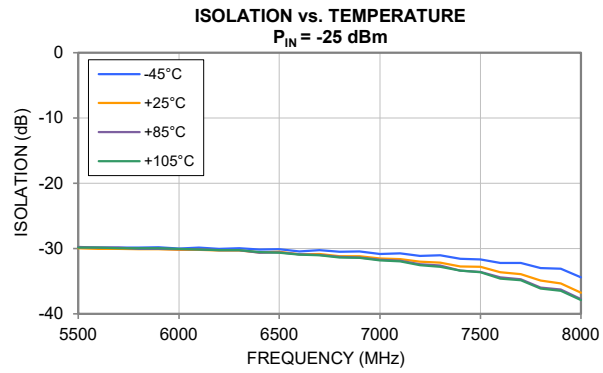
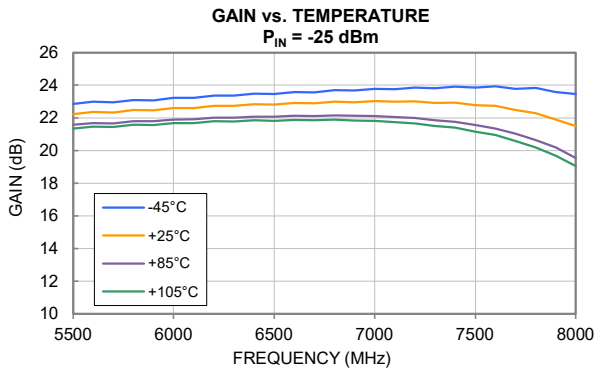
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-83LNC+ (Figure 2). All data taken at nominal conditions $V_{EN} = V_{DD} = +6\text{ V}$, and $R_{I,ADJ} = \text{Open}$ unless noted otherwise.



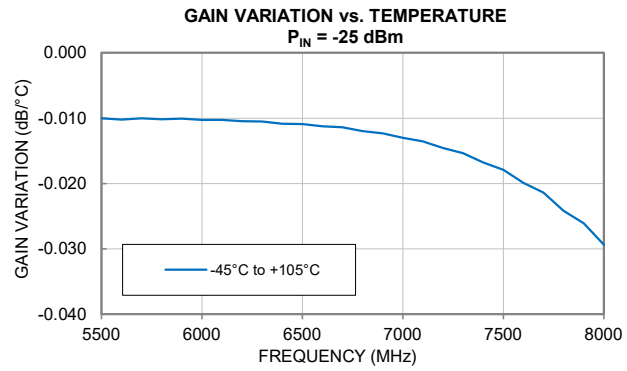
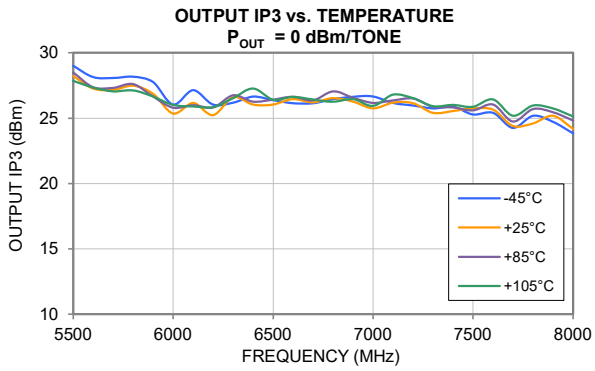
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-832LNC+ (Figure 3). All data taken at nominal conditions $V_{EN} = V_{DD} = +6\text{ V}$ and $RI_{ADJ} = \text{Open}$ unless noted otherwise.



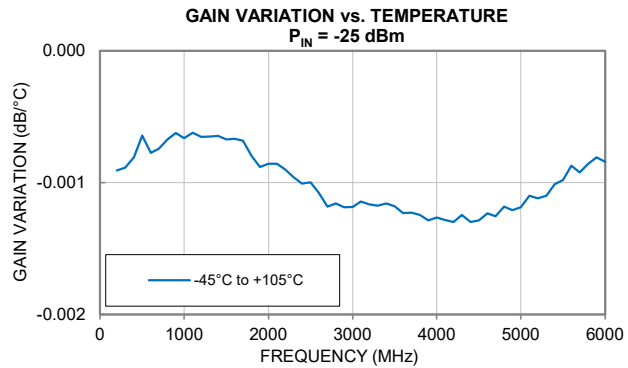
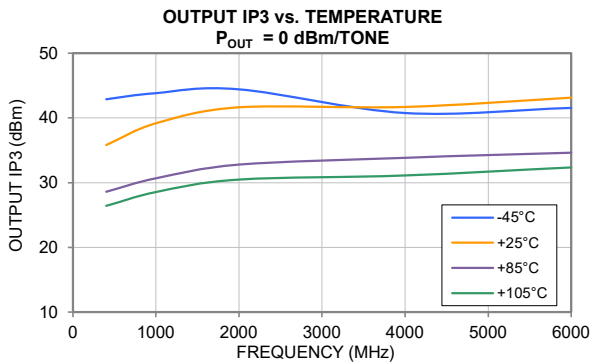
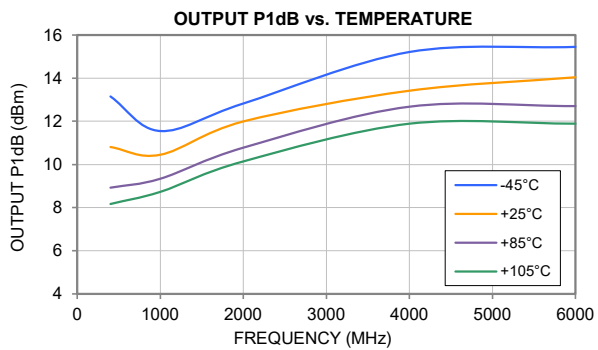
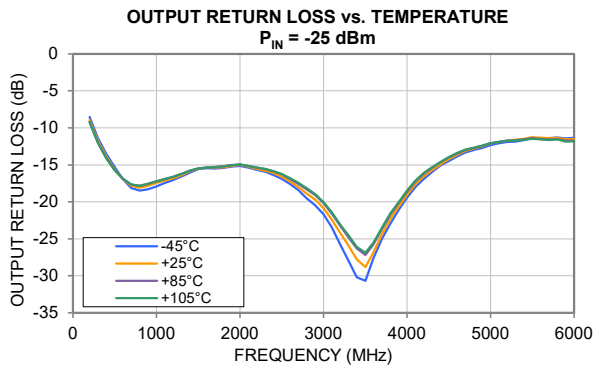
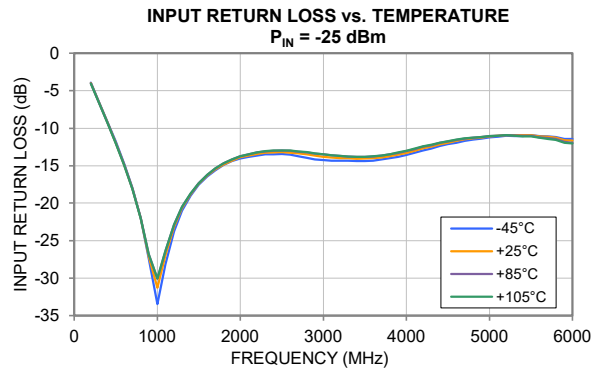
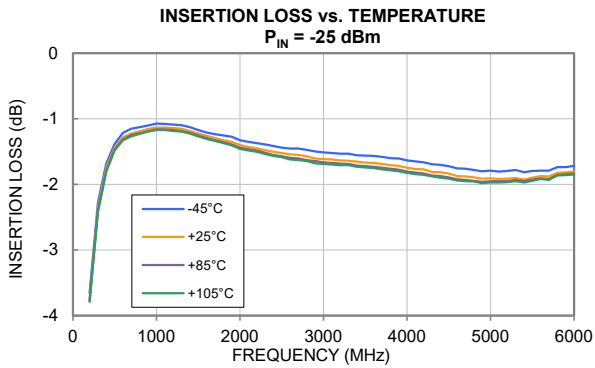
Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-832LNC+ (Figure 3). All data taken at nominal conditions $V_{EN} = V_{DD} = +6$ V and $R_{I,ADJ} = \text{Open}$ unless noted otherwise.



Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-83LNC+ (Figure 2). All data taken at nominal conditions $V_{EN} = 0$ V, $V_{DD} = +6$ V, and $RI_{ADJ} =$ Open unless noted otherwise.



Typical Performance Curves

Note: The following data was taken on the Mini-Circuits Characterization Test Board TB-TSY-832LNC+ (Figure 3). All data taken at nominal conditions $V_{EN} = 0$ V, $V_{DD} = +6$ V, and $R_{ADJ} = \text{Open}$ unless noted otherwise.

