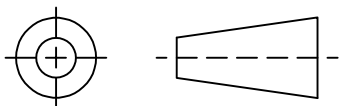


THIRD ANGLE PROJECTION

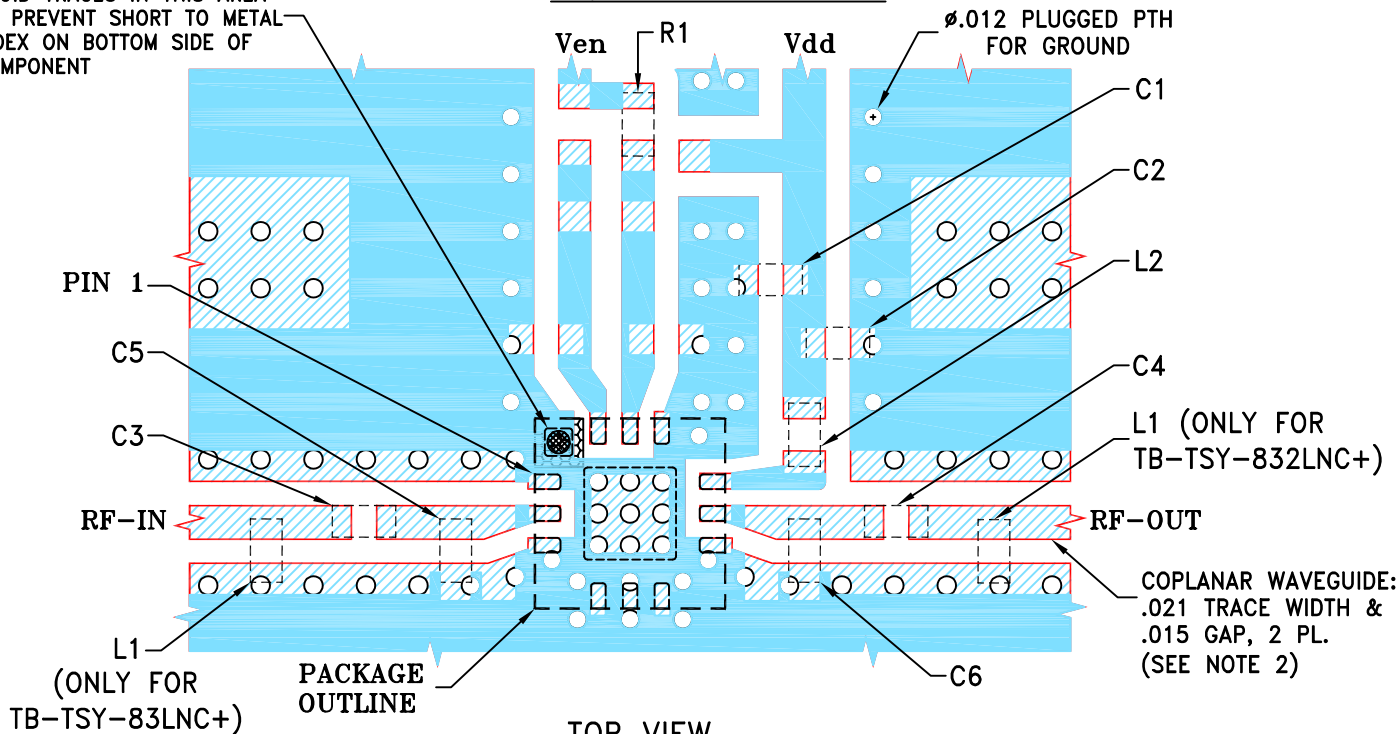


REVISIONS

REV	ECN No.	DESCRIPTION	DATE	DR	AUTH
OR	ECO-020666	NEW RELEASE	01/29/24	ITG	IL

SUGGESTED MOUNTING CONFIGURATION FOR DQ1225 CASE STYLE

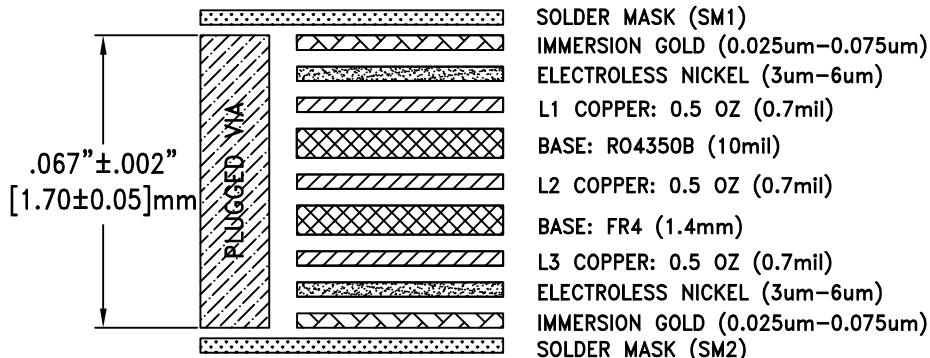
KEEP-OUT ZONE, .030X.030, AVOID TRACES IN THIS AREA TO PREVENT SHORT TO METAL INDEX ON BOTTOM SIDE OF COMPONENT



TOP VIEW

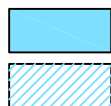
3 LAYER STACK-UP DIAGRAM

COMPONENT	SIZE
C1...C6	0402
L1,L2	0402
R1	0402



NOTES:

1. PCB IS MULTILAYER PCB, SEE STACK-UP DIAGRAM.
2. TRACE WIDTH & GAP PARAMETERS ARE SHOWN FOR ROGERS RO4350B WITH DIELECTRIC THICKNESS .010" ± .001"; COPPER: 1/2 OZ. FOR OTHER MATERIALS TRACE WIDTH AND GAP MAY NEED TO BE MODIFIED.
3. CHIP COMPONENT FOOT PRINTS SHOWN FOR REFERENCE. FOR COMPONENT VALUES REFER TO TB-TSY-83LNC+ OR TB-TSY-832LNC+.
4. COPPER LAYERS L2 & L3 OF THE PCB ARE CONTINUOUS GROUND PLANES.



SOLID BLUE DENOTES PCB COPPER LAYOUT WITH SMOBC (SOLDER MASK OVER BARE COPPER)
 HATCHED BLUE DENOTES COPPER LAND PATTERN FREE OF SOLDER MASK

UNLESS OTHERWISE SPECIFIED	INITIALS		DATE
DIMENSIONS ARE IN INCHES	DRAWN	ITG	01/29/24
TOLERANCES ON:	CHECKED	GF	01/29/24
2 PL DECIMALS ±	APPROVED	IL	01/29/24
3 PL DECIMALS ± .005			
ANGLES ±			
FRACTIONS ±			



Mini-Circuits®

13 Neptune Avenue
Brooklyn NY 11235

PL, DQ1225, TB-TSY-83LNC+

SIZE	CODE IDENT	DRAWING NO:	REV:
A	15542	98-PL-775	OR
FILE:	98PL775	SCALE: 8:1	SHEET: 1 OF 1

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