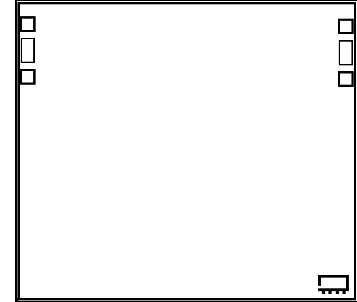
MMIC DIE

X3 Frequency Multiplier CY3-223-D+

50Ω Output 10 to 22 GHz

THE BIG DEAL

- Ultra-wideband, output from 10 to 22 GHz
- Wide input power range, +12 to +18 dBm
- Low Conversion Loss, 17 dB Typ.
- Good Fundamental and Harmonic Suppression: F1 > +34 dBc; F2 > +45 dBc; F4 > +50 dBc.



+RoHS Compliant
The +Suffix identifies RoHS Compliance.
See our website for methodologies and qualifications

SEE ORDERING INFORMATION ON THE LAST PAGE

APPLICATIONS

- 5G MIMO and Back Haul Radio Systems
- Satellite Communications
- Test and Measurement Equipment
- Radar, EW, and ECM Defense Systems

PRODUCT OVERVIEW

Mini-Circuits' CY3-223-D+ is an ultra-wideband MMIC Frequency Tripler, converting input frequencies from 3.33 to 7.33 GHz into output frequencies from 10 to 22 GHz. Its wide output range makes this model suitable for broadband systems as well as a wide variety of narrow-band applications. The CY3-223-D+ die utilizes GaAs HBT technology and is suitable for chip and wire assemblies.

KEY FEATURES

Feature	Advantages
Broadband, 10 to 22 GHz output	With an output frequency range spanning 10 to 22 GHz, this multiplier supports broadband applications such as defense and instrumentation as well as a wide range of narrowband system requirements including 5G.
Excellent fundamental and harmonic suppression: • F1, +30 dBc • F2, +45 dBc • F4, +50 dBc	Reduces spurious signals and the need for additional filtering.
Unpackaged Die	Enable user to integrate it directly into hybrids.
Wide input power range, +12 to +18 dBm	Wide input power signal range accommodates different input signal levels while still maintaining a low Conversion Loss.

REV. A
ECO-018288
CY3-223-D+
MCL NY
230711

ELECTRICAL SPECIFICATIONS[1] AT 25°C AND Zo = 50Ω, UNLESS NOTED OTHERWISE

Parameter		Condition (GHz)	RF Input = +12 dBm			Unit
			Min.	Typ.	Max.	
Multiplication Factor				3		
Frequency Range, Input (F1)			3.33		7.33	GHz
Frequency Range, Output (F2)			10	-	22	GHz
Input Power			+12		+18	dBm
Conversion Loss		3.33		22.0		dB
		4		18.0		
		5		16.4		
		6		18.2		
		7.33		22.1		
Harmonic Output[2]	F1	3.33		45.8		dBc
		4		49.8		
		5		46.6		
		6		44.9		
		7.33		22.1		
	F2	3.3		86.1		dBc
		4		71.9		
		5		55.1		
		6		45.0		
		7.33		39.9		
	F3	3.33		47.9		dBc
		4		48.9		
		5		56.2		
		6		51.7		
		7.33		40.9		

1. Die soldered in a 4x4 mm 24L MCLP and measured on Mini-Circuits Characterization Test Board TB-CY3-223C+.
2. Harmonics of input frequency below the power level of F3.

MAXIMUM RATINGS[3]

Parameter	Ratings
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C
RF Input Power	+22 dBm (5 minute max)
	+19 dBm (Continuous)

3. Permanent damage may occur if any of these limits are exceeded.

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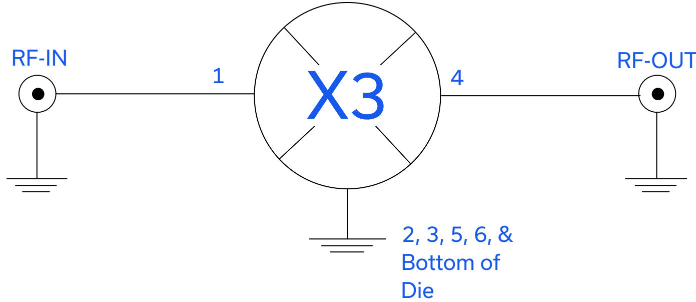
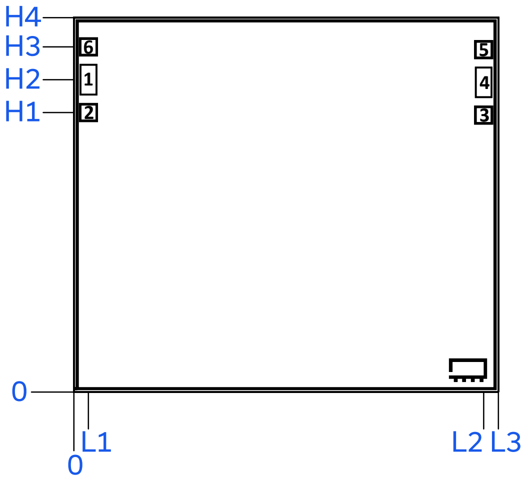
SIMPLIFIED SCHEMATIC AND PAD DESCRIPTION



Function	Pad Number	Description
RF-IN	1	RF-Input Pad.
RF-OUT	4	RF-Output Pad.
Ground	2, 3, 5, 6 & Bottom of Die	The bond pads are connected to back-side through vias and do not require wire-bond connections to ground.

BONDING PAD POSITION

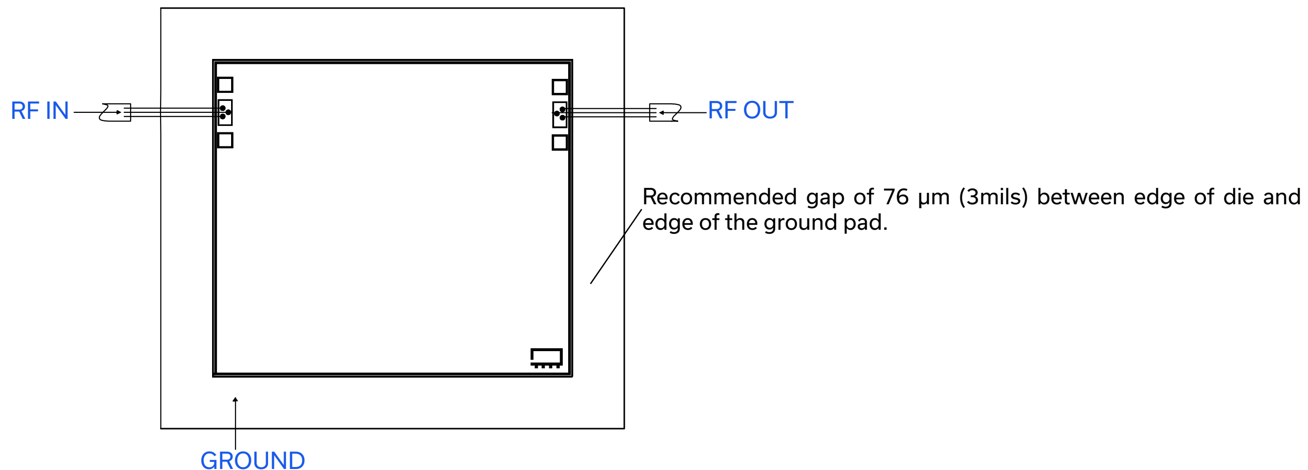


DIMENSIONS IN µM, TYPICAL

L1	L2	L3	H1	H2	H3	H4
85	2375	2460	1620	1810	2000	2170

Thickness	Die Size	Pad Size 1 & 4	Pad Size 2, 3, 5, & 6
100	2460 x 2170	92 x 172	92 x 92

ASSEMBLY DIAGRAM



1. Recommended bond length for RF-IN & RF-Out: 305 µm (12mils)
2. Recommended amount of bonding wires per port: 3

APPLICATION AND CHARACTERIZATION CIRCUIT

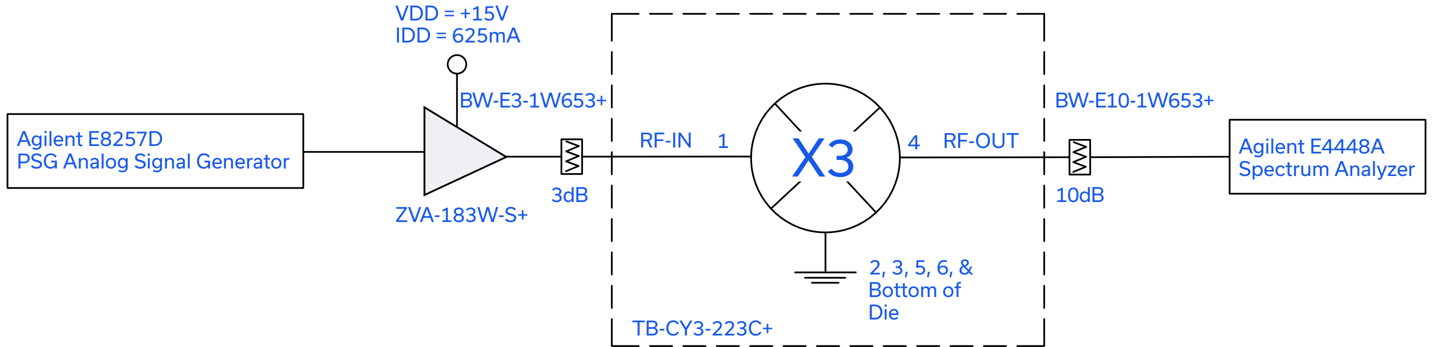
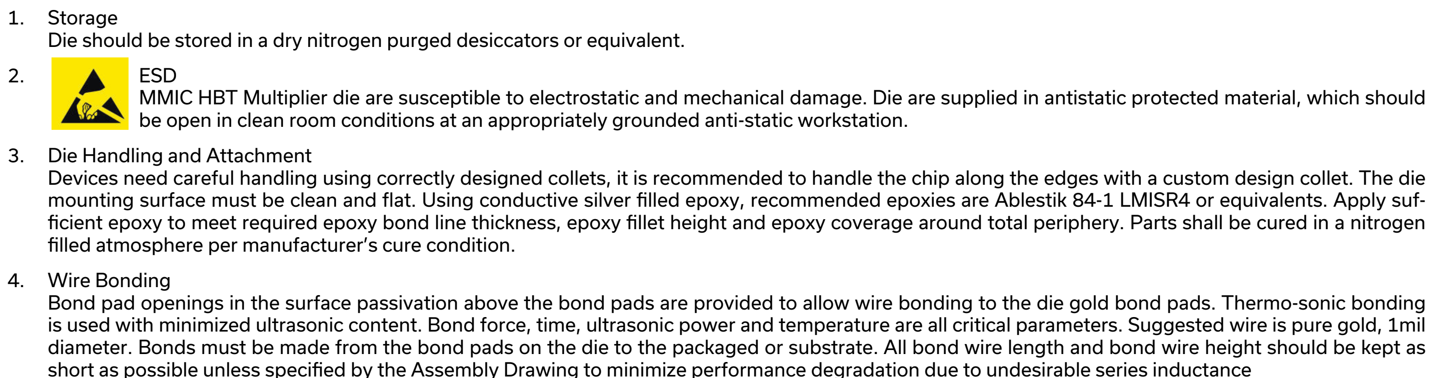


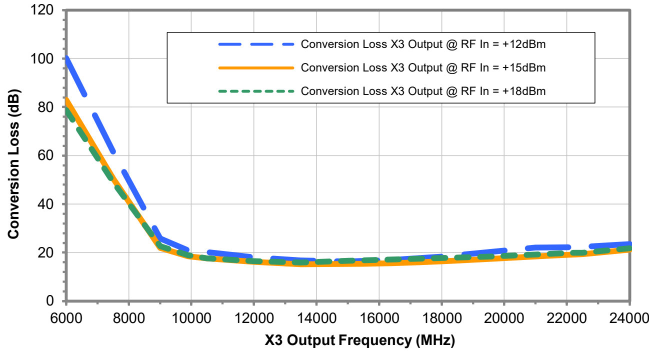
Fig 1. Application and Characterization Circuit
Note: This block diagram is used for characterization.
(DUT is soldered onto a 4x4 24L MCLP and measured on Mini-Circuits Characterization test board TB-CY3-223C+) Conversion Loss and Harmonic Output are measured using Agilent E4448A PSA Spectrum Analyzer

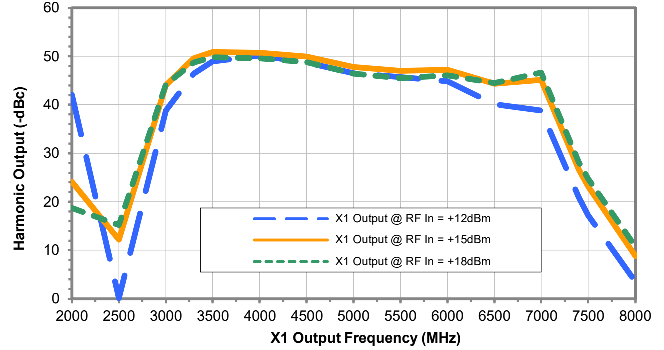
ASSEMBLY PROCEDURE

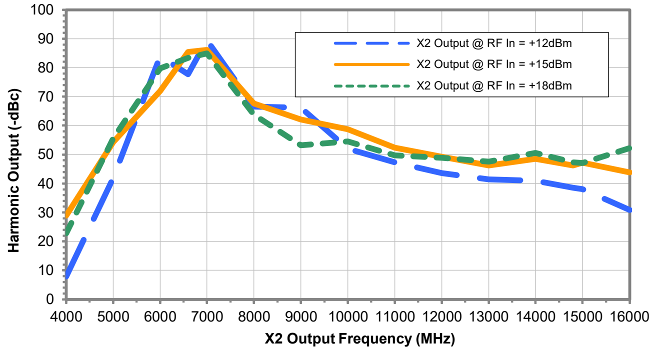


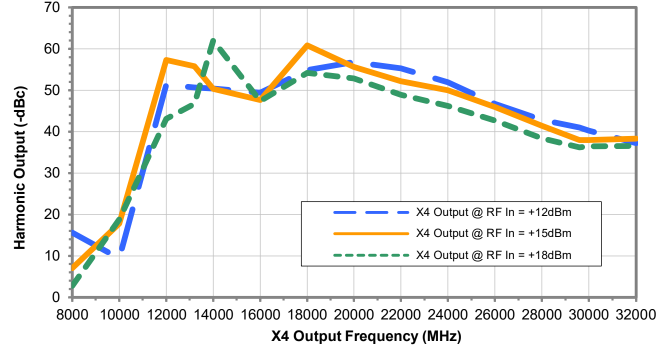
1. Storage
   Die should be stored in a dry nitrogen purged desiccators or equivalent.
2. ESD
   MMIC HBT Multiplier die are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be open in clean room conditions at an appropriately grounded anti-static workstation.
3. Die Handling and Attachment
   Devices need careful handling using correctly designed collets, it is recommended to handle the chip along the edges with a custom design collet. The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are Ablestik 84-1 LMISR4 or equivalents. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition.
4. Wire Bonding
   Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the die gold bond pads. Thermo-sonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1mil diameter. Bonds must be made from the bond pads on the die to the packaged or substrate. All bond wire length and bond wire height should be kept as short as possible unless specified by the Assembly Drawing to minimize performance degradation due to undesirable series inductance

TYPICAL PERFORMANCE CURVES









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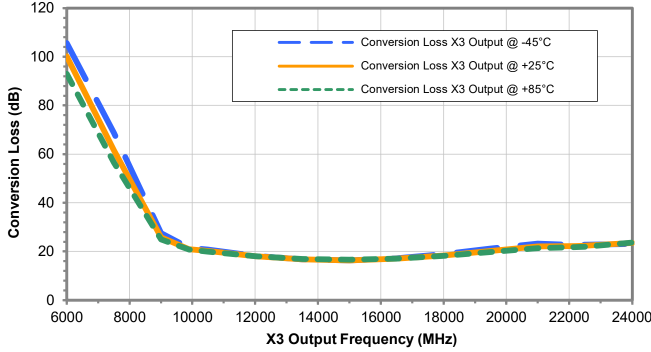

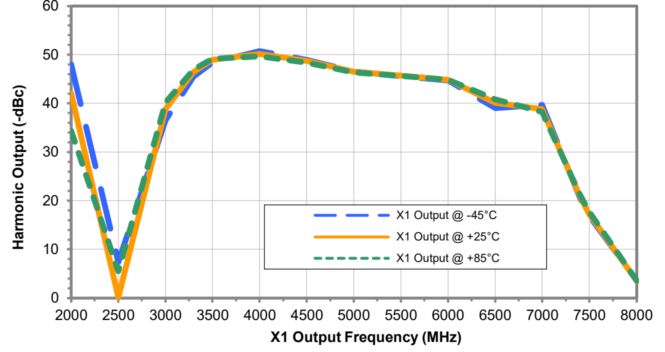

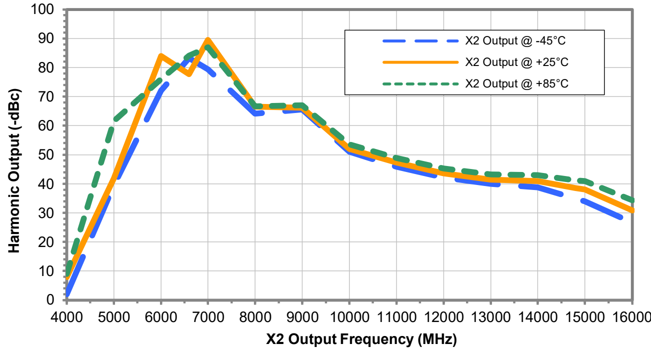

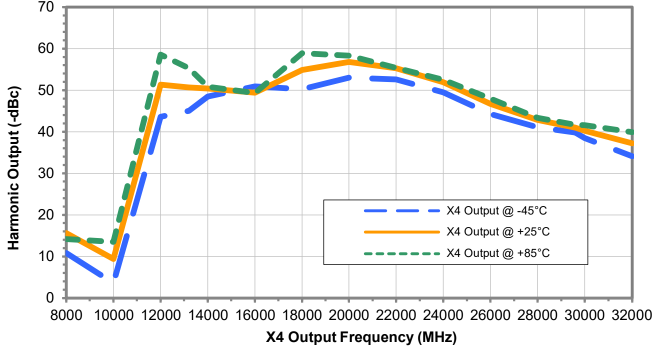

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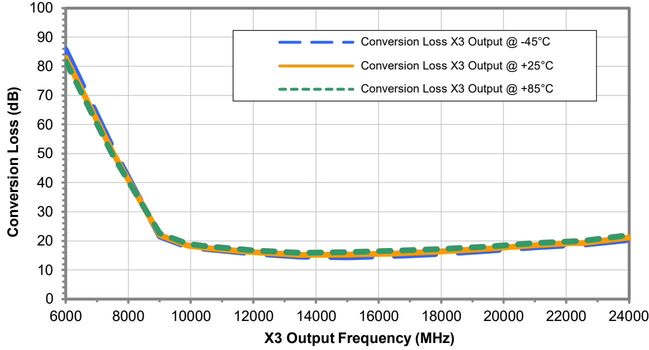

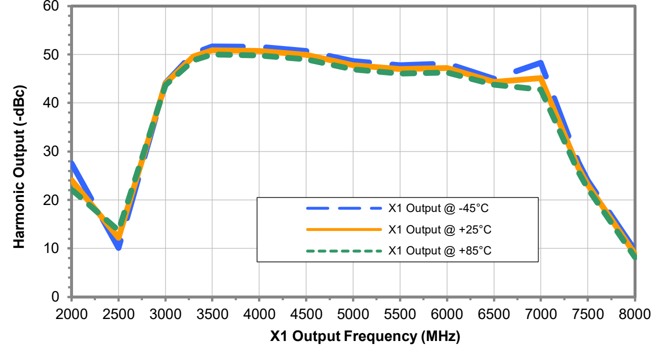

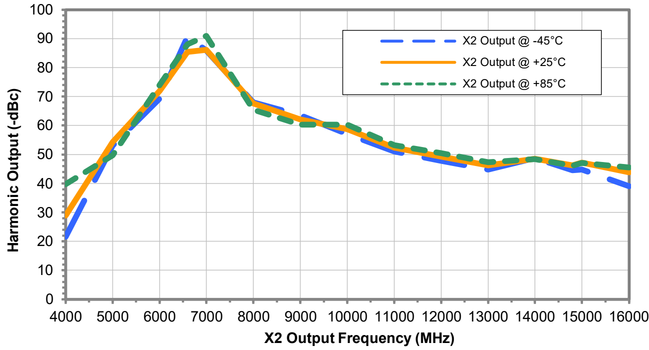

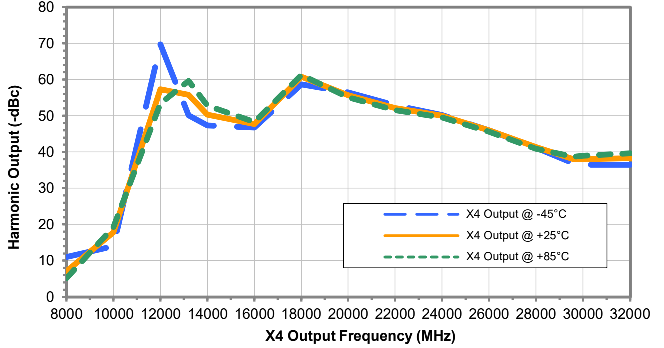

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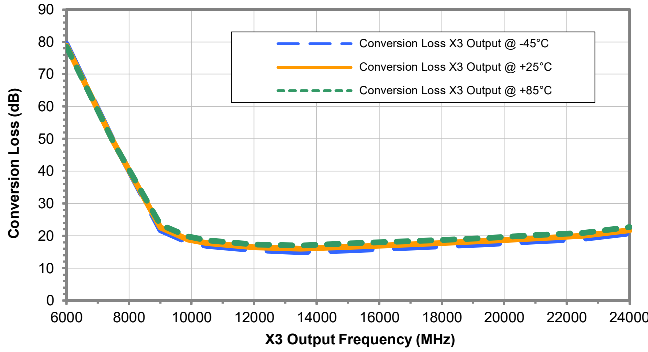

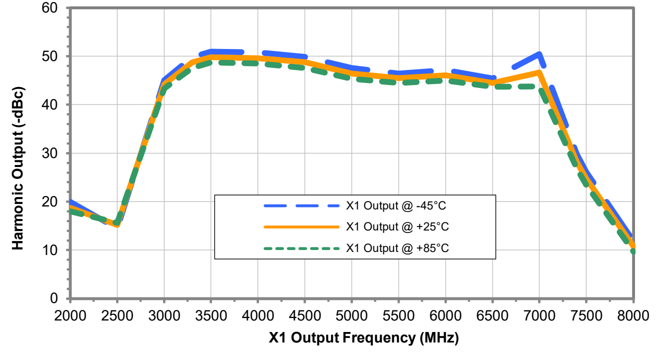

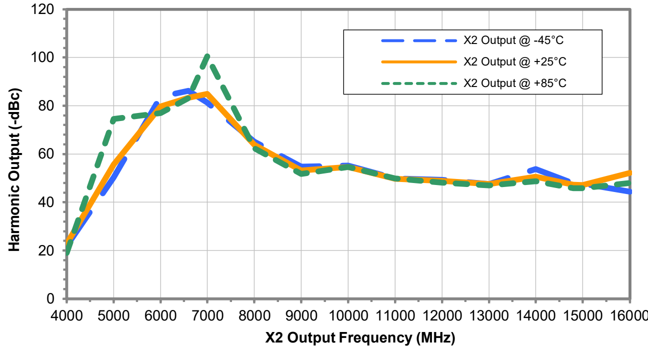

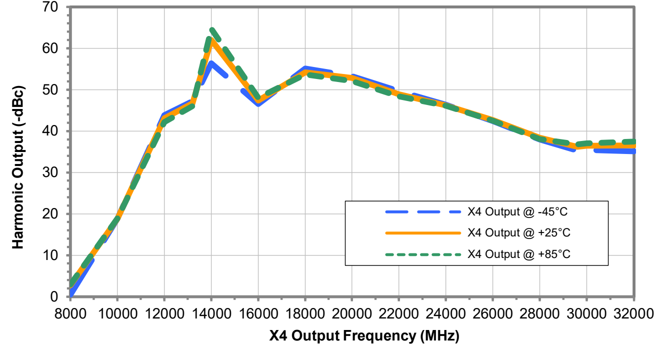

ADDITIONAL DETAILED TECHNICAL INFORMATION IS AVAILABLE ON OUR DASH BOARD.

Performance Data	Data Table Swept Graphs	
Case Style	Die	
Die Ordering and packaging information	Quantity, Package Gel – Pak: 5, 10, 50 Medium†, Partial wafer: KGD*<440 Full Wafer	Model No. CY3-223-DG+ CY3-223-DP+ CY3-223-DF+
Die Marking	EL-MUL-2	
Environmental Ratings	ENV80	

*Known Good Die ('KGD') means that the die in question have been subjected to Mini-Circuits DC test performance criteria and measurement instructions and that the parametric data of such die fall within a predefined range. While DC testing is not definitive, it does provide a higher degree of confidence that die is capable of meeting typical RF electrical performance specified by Mini-Circuits.

NOTES

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