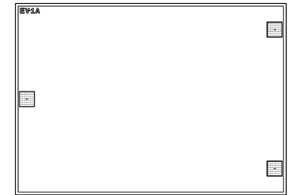
MMIC

Power Splitter/Combiner Die

EP2K-D+

2 Way-0°   50Ω   2 to 26.5 GHz



The Big Deal

- Ultra-Wide Bandwidth, usable over 1.8 to 28 GHz
- High Power Handling, 2.5W as a Splitter

Product Overview

Mini-Circuits EP2K-D+ is a MMIC splitter/combiner die designed for wideband operation from 2 to 26.5 MHz. This model provides excellent power ratings up to 2.5W power handling (as a splitter) and up to 1.2A DC current handling. Manufactured using GaAs IPD technology, it provides a high level of ESD protection and excellent reliability.

Key Features

Feature	Advantages
Wideband, 1.8 to 28 GHz	One power splitter can be used in many applications, saving component count. Also ideal for wideband applications such as military and instrumentation.
Excellent power handling 2.5W as a splitter 1.7W internal dissipation as a combiner	In power combiner applications, half the power is dissipated internally. EP2K-D+ is designed to handle 1.7W internal dissipation as a combiner allowing reliable operation without excessive temperature rise. Similar splitters implemented as Wilkinson splitters on PCB require big resistors and additional heat sinking. As a splitter, EP2K-D+ can handle up to 2.5W in a very small package.
DC Passing up to 1.2A	DC current passing is helpful in applications where both RF & DC need to pass through the DUT, such as antenna mounted hardware.
Unpackaged die	Enables user to integrate it directly into hybrids.

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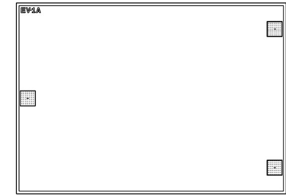


Features
- Wide bandwidth, 2 to 26.5 GHz, usable over 1.8 to 28 GHz
- Excellent amplitude unbalance, 0.1 dB typ.
- Good phase unbalance, 1.5 to 8.5 deg. typ.
- High ESD level*
- DC passing

Applications
- WIMAX
- ISM
- Instrumentation
- Radar
- WLAN
- Satellite communications
- LTE

+RoHS Compliant
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

Ordering Information: Refer to Last Page

Electrical Specifications at 25°C[1]

Parameter	Frequency (GHz)	Min.	Typ.	Max.	Unit
Frequency Range		2		26.5	GHz
Insertion Loss, above 3.0 dB[2]	2 - 5 5 - 10 10 - 18 18 - 26.5		0.8 0.9 1.6 2.1		dB
Isolation	2 - 5 5 - 10 10 - 18 18 - 26.5		9.5 18.0 18.9 15.9		dB
Phase Unbalance	2 - 5 5 - 10 10 - 18 18 - 26.5		1.5 2.9 6.0 8.5		Degree
Amplitude Unbalance	2 - 5 5 - 10 10 - 18 18 - 26.5		0.1 0.1 0.2 0.3		dB
VSWR (Port S)	2 - 5 5 - 10 10 - 18 18 - 26.5		1.6 1.2 1.3 1.5		:1
VSWR (Port 1-2)	2 - 5 5 - 10 10 - 18 18 - 26.5		1.7 1.2 1.4 1.6		:1

1. Tested on Mini-Circuits die characterization test board.

Maximum Ratings[2,3]

Parameter	Ratings
Operating Temperature	-40°C to 85°C
Power Input (as a splitter)	2.5W max. at 25°C. Derate linearly to 1.25W at 85°C
Internal Dissipation	1.7W max. at 25°C. Derate linearly to 1.1W at 85°C
DC Current	1.2A max. at 25°C. Derate linearly to 0.6A at 85°C

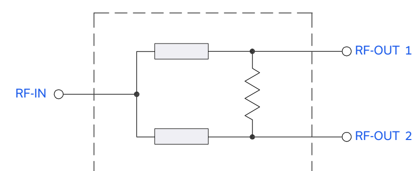
2. Permanent damage may occur if any of these limits are exceeded.
3. Die performance is measured in industry standard 4x4mm 24-lead MCLP package.

* ESD rating
Human body model (HBM): Class 2(1800 to <4000 V) in accordance with ANSI/ESD 5.1-2007
Machine model (MM): Class M3 (200 to <400 V) in accordance with ANSI/ESD 5.2-2009

Pad Connections

Pad	Description
RF IN	RF-IN as splitter / RF-OUT as combiner
RF OUT1	RF-OUT1 as splitter / RF-IN1 as combiner
RF OUT2	RF-OUT1 as splitter / RF-IN1 as combiner

Simplified Electrical Schematic



Die Layout

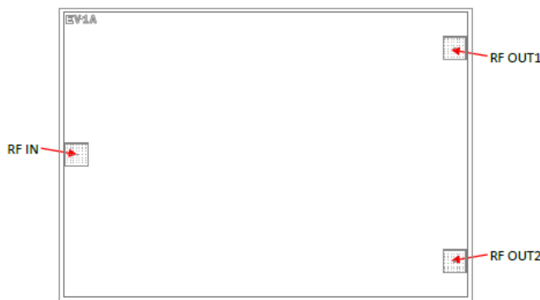


Fig 2. Die Layout

Bonding Pad Position

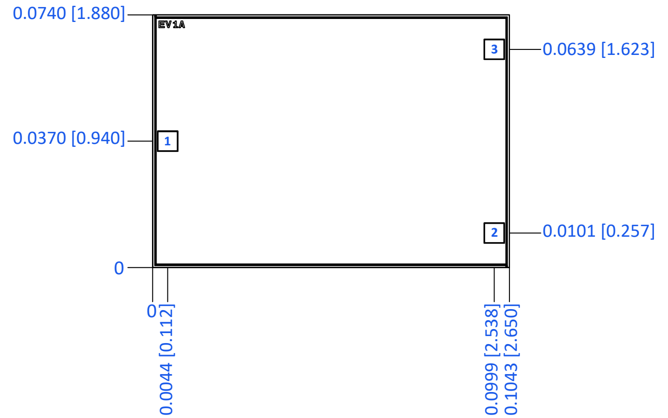


Fig 3. Bonding Pad Positions

DIMENSION: inches [mm], Typical

Die Size	0.1043 x 0.0740 [2.650 x 1.880]
Die Thickness	0.0079 [0.200]
Bond Pad Size Pad 1,2,3	0.0056 x 0.0056 [0.142 x 0.142]
Plating (Pads & bottom of Die)	Gold

Assembly and Handling Procedure

1. Storage
   Dice should be stored in a dry nitrogen purged desiccators or equivalent.
2. ESD
   MMIC amplifier dice are susceptible to electrostatic and mechanical damage. Die are supplied in antistatic protected material, which should be opened in clean room conditions at an appropriately grounded anti-static worksta tion. Devices need careful handling using correctly designed collets, vacuum pickup tips or sharp antistatic tweezers to deter ESD damage to dice.
3. Die Attach
   The die mounting surface must be clean and flat. Using conductive silver filled epoxy, recommended epoxies are DieMat DM6030HK-PT/H579 or Ablestik 84-1LMISR4. Apply sufficient epoxy to meet required epoxy bond line thickness, epoxy fillet height and epoxy coverage around total die periphery. Parts shall be cured in a nitrogen filled atmosphere per manufacturer's cure condition. It is recommended to use antistatic die pick up tools only.
4. Wire Bonding
   Bond pad openings in the surface passivation above the bond pads are provided to allow wire bonding to the dice gold bond pads. Thermosonic bonding is used with minimized ultrasonic content. Bond force, time, ultrasonic power and temperature are all critical parameters. Suggested wire is pure gold, 1 mil diameter. Bonds must be made from the bond pads on the die to the package or substrate. All bond wires should be kept as short as low as reasonable to minimize performance degradation due to undesirable series inductance.

Assembly Diagram

Four 1 mil bond wires should be used for RF input and RF output.

Recommended Wire Length, Typical

Wire	Wire Length (mm)	Wire Loop Height (mm)
RF IN, RF OUT1, RF OUT2	0.40	0.15

Additional Detailed Technical Information

additional information is available on our dash board.

Performance Data	Data Table	
	Swept Graphs	
	S-Parameter (S3P Files) Data Set with and without port extension(.zip file)	
Case Style	Die	
Die Ordering and packaging information (Note 3)	Quantity, Package	Model No.
	Small, Gel - Pak: 5,10,50	EP2K-DG+
	Medium†, Partial Wafer: 350	EP2K-DP+
	Large†, Full Wafer	EP2K-DF+
	†*Available upon request contact sales representative* Refer to AN-60-067	
Environmental Ratings	ENV-80	

3. Dice taken from PCM good wafer. No RF or DC test performed.

ESD Rating**

Human Body Model (HBM): Class 2 (1800 to <4000V) in accordance with ANSI/ESD STM 5.1 - 2007

Machine Model (MM): Class M3 (200 to <400V) in accordance with ANSI/ESD STM5.2-1999

** Tested in industry standard 4X4mm, 24-Lead MCLP package.

Additional Notes

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